

Boolean Satisfiability and SAT Solvers

Philippe Suter

SAV, April 16th, 2013

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- S. Cook, *The complexity of theorem proving procedures*, STOC 1971.

SAT in Practice

- Ubiquitous in hardware/circuit design
 - E.g. equivalence checking.
- Search/AI problems
 - E.g. reduce Sudoku to SAT.
 - Dependency management in Eclipse.
- Software verification
 - By itself, and as part of the SMT stack.

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- Obviously not very efficient. SAT solving is all about making this enumeration “smart”.

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$$(a + \bar{b} + c)(\bar{a} + c + d + \bar{e})(b + \bar{d} + e)$$

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Note that a truth table is a kind of *disjunctive* normal form (DNF).

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 - ...but that introduces an exponential blowup...
 - ...and you might as well convert to DNF, then.
- Instead, we use an encoding based on *introducing new variables*.

Tseitin's Encoding

- Idea: rewrite φ into ψ that is *equisatisfiable*.

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The diagram shows the formula $\varphi \equiv (a \wedge (\neg b \vee (c \wedge d)))$. A blue bracket above the subformula $(c \wedge d)$ is labeled p_2 . A blue bracket below the subformula $(c \wedge d)$ is labeled p_1 .

$$p_1 \Leftrightarrow c \wedge d$$

$$\neg p_1 \vee c$$

$$\neg p_1 \vee d$$

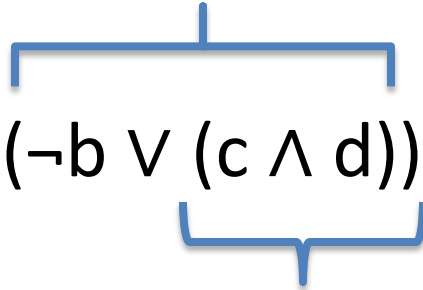
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$$\neg p_2 \vee \neg b \vee p_1$$

$$b \vee p_2$$

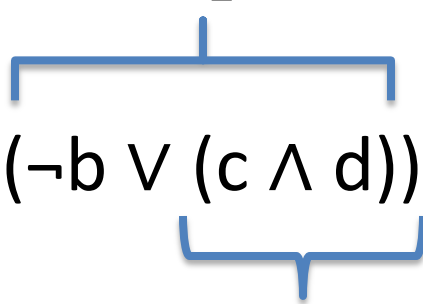
$$\neg p_1 \vee p_2$$

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$$\psi \equiv a \wedge p_2$$

$$p_1 \Leftrightarrow c \wedge d$$

$$\neg p_1 \vee c$$

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$$p_2 \Leftrightarrow \neg b \vee p_1$$

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Solving clauses: 2-SAT

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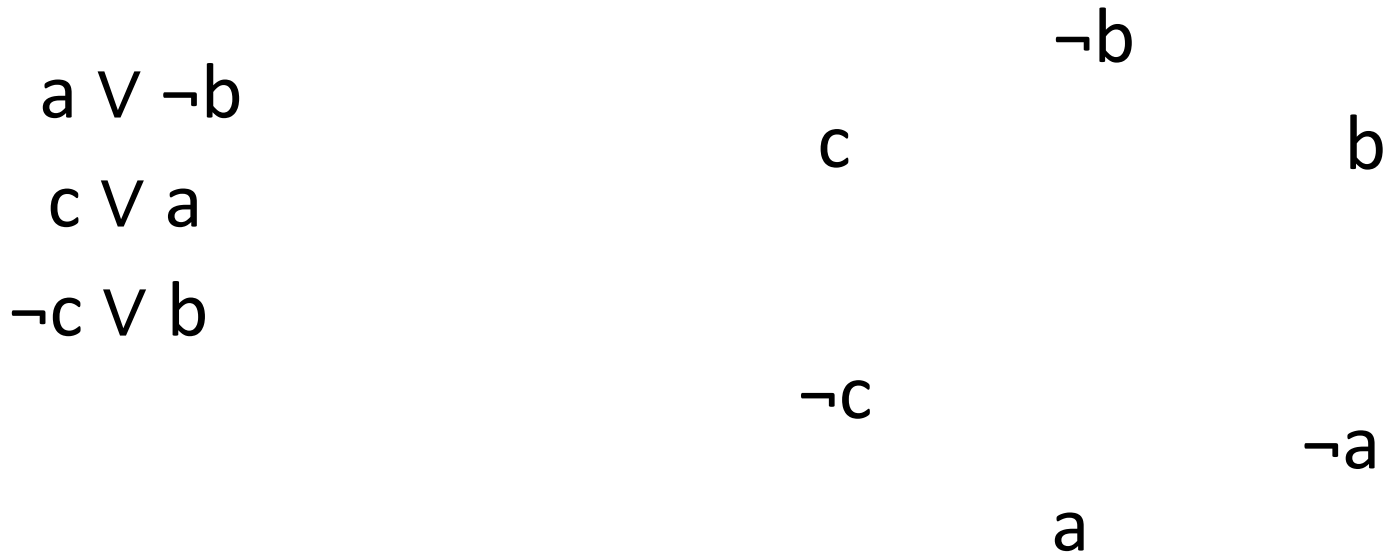
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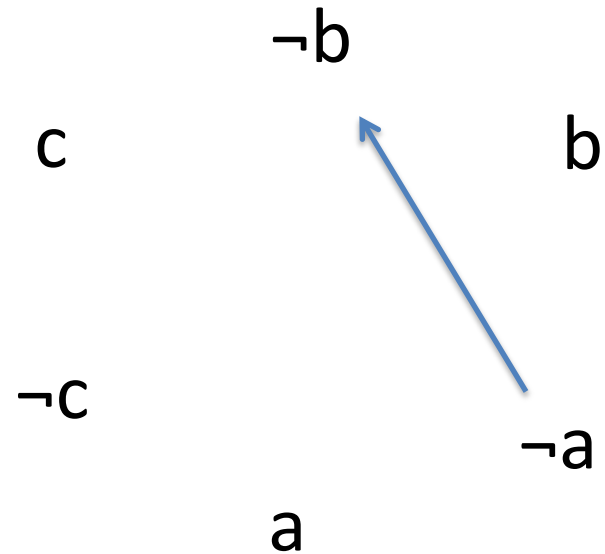
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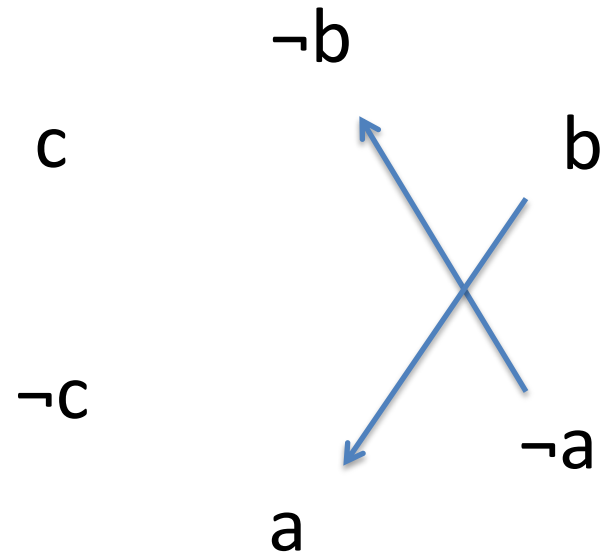
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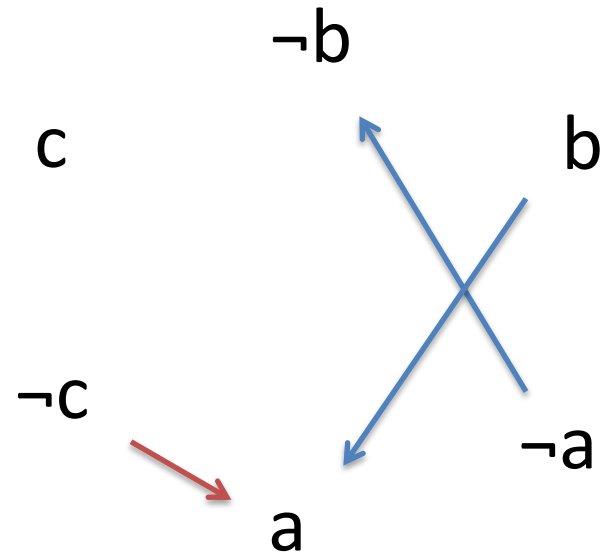
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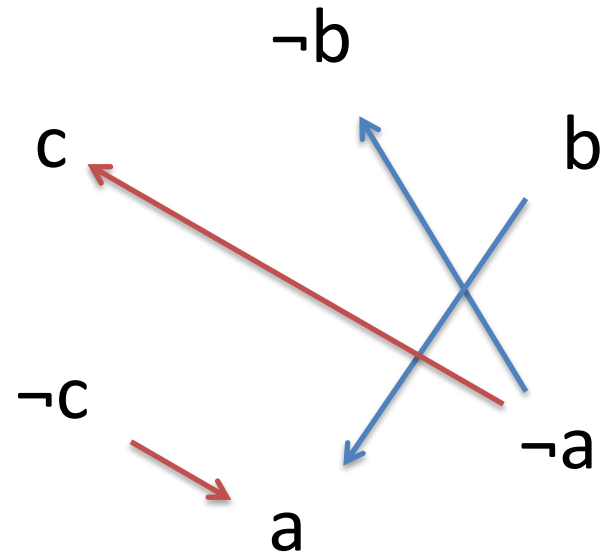
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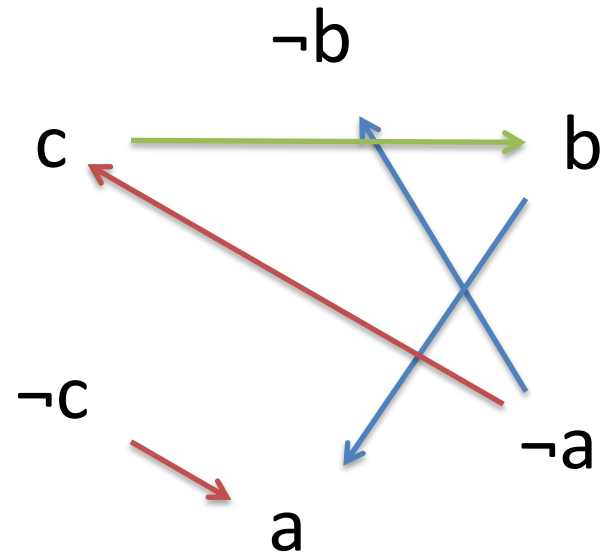
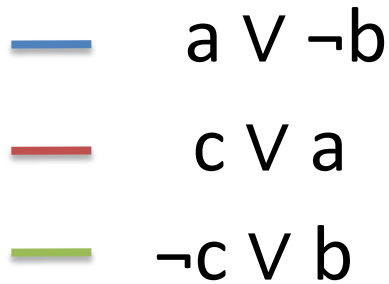
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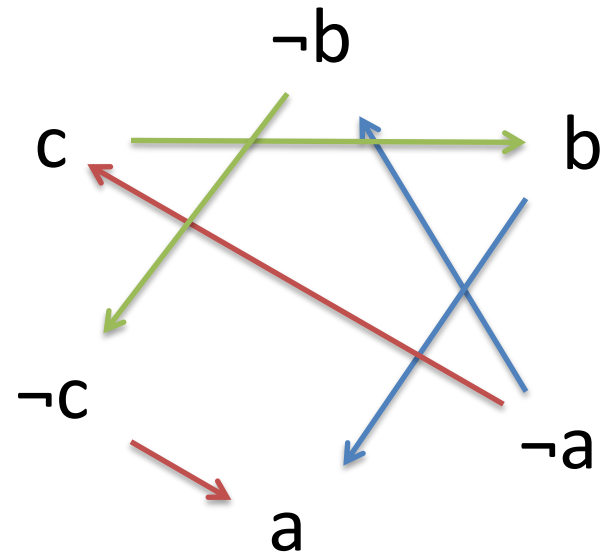
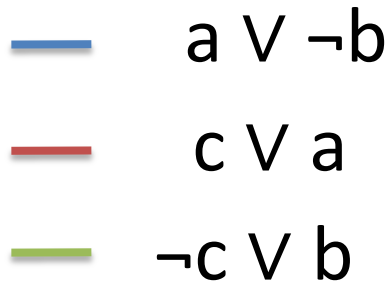
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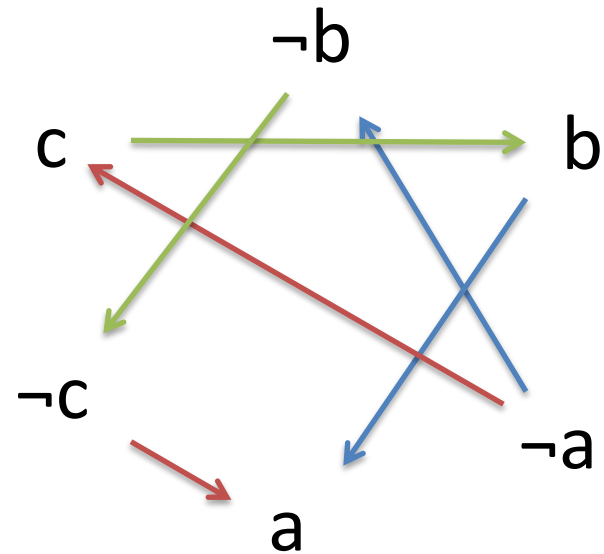
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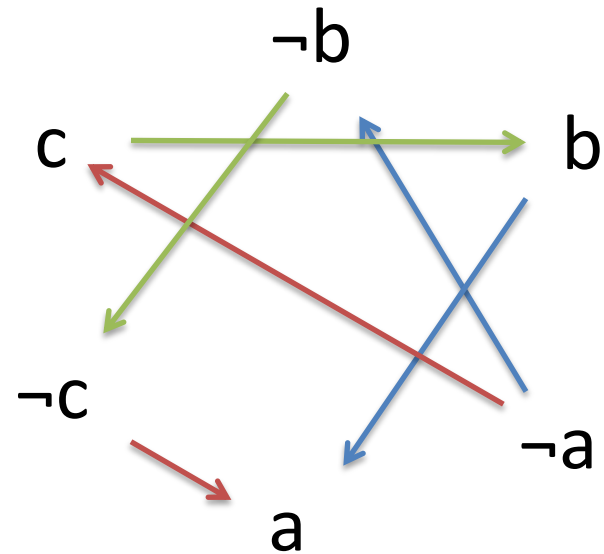
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You can solve 2-SAT in polynomial time.

Some of the techniques for 2-SAT are used in general SAT solvers.

3-SAT

- NP-complete.
 - Reduction from SAT: split longer clauses using fresh variables.

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- (Not so relevant to SAT solving technology.)

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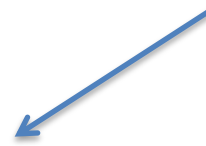
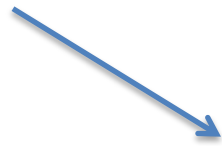
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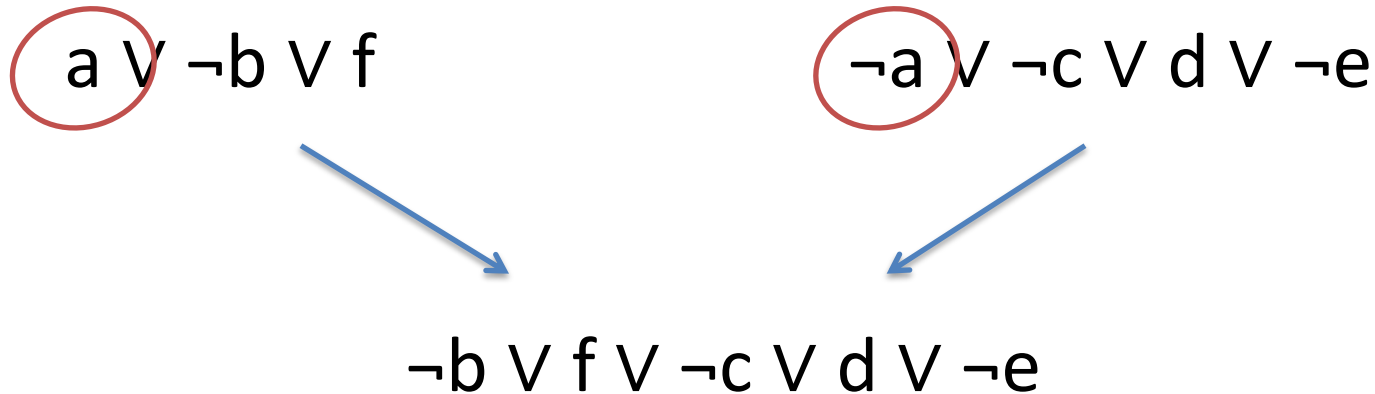
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First Approach: Resolution



- Resolution eliminates one variable by producing a new clause (*resolvent*) from complementary ones.

Resolution

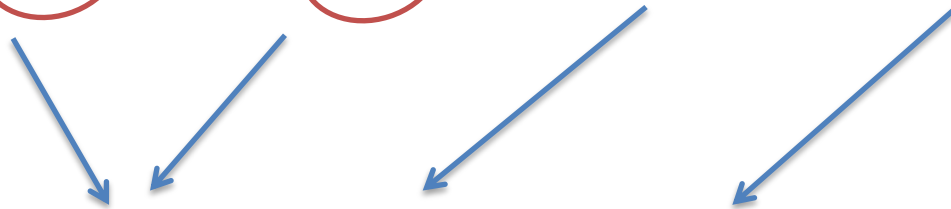
$$(a \vee b) \wedge (a \vee \neg b) \wedge (\neg a \vee c) \wedge (\neg a \vee \neg c)$$

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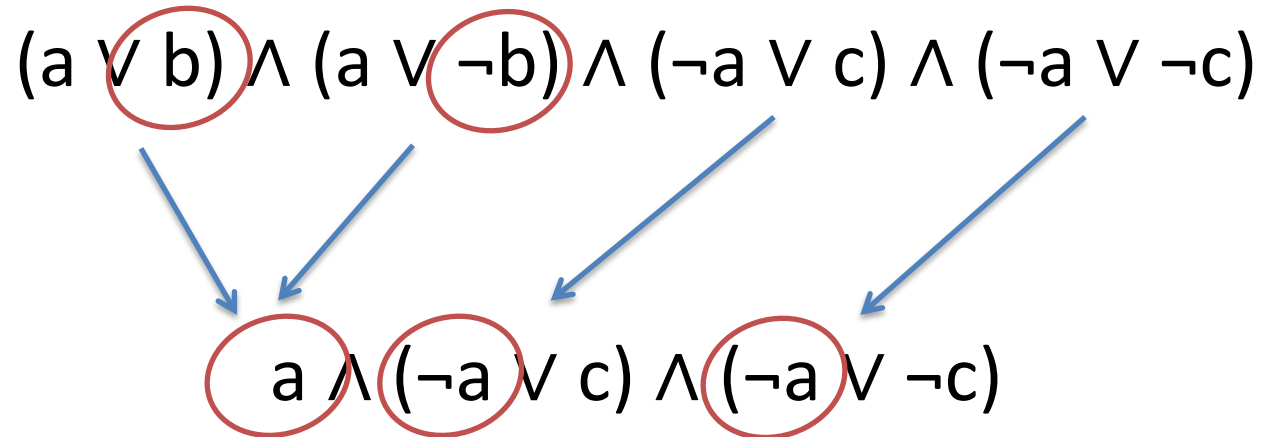
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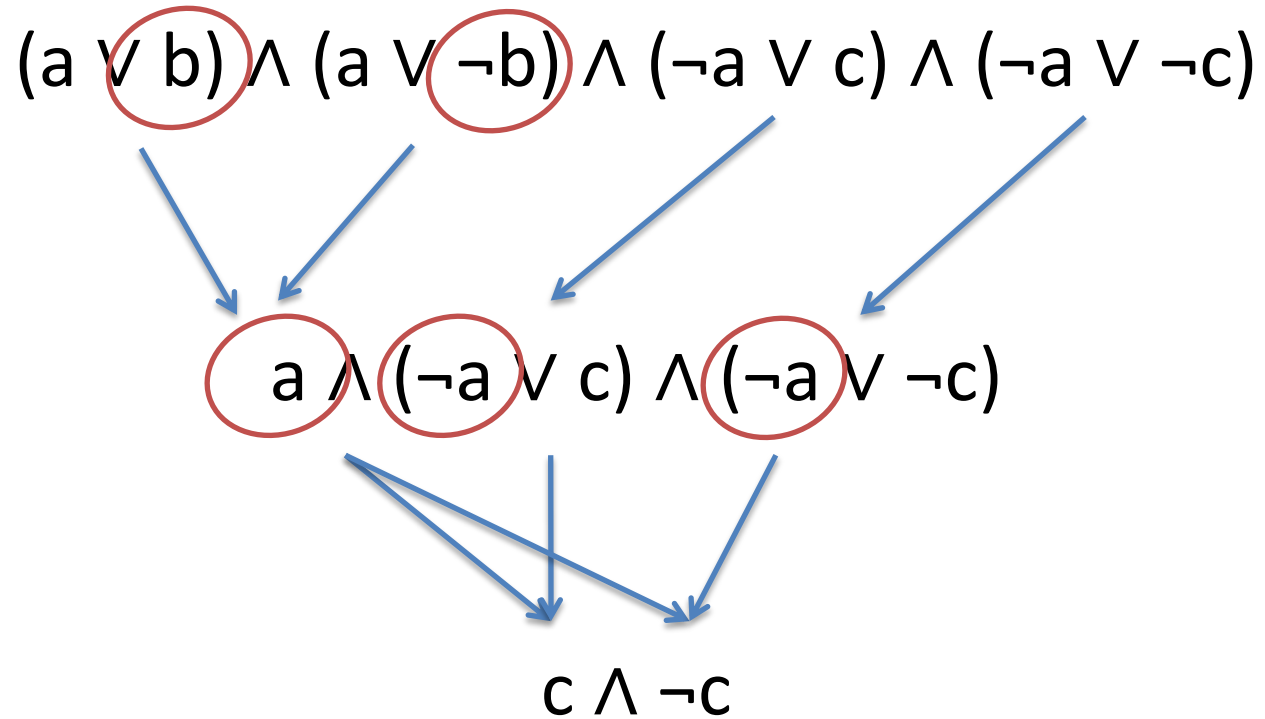


$$a \wedge (\neg a \vee c) \wedge (\neg a \vee \neg c)$$

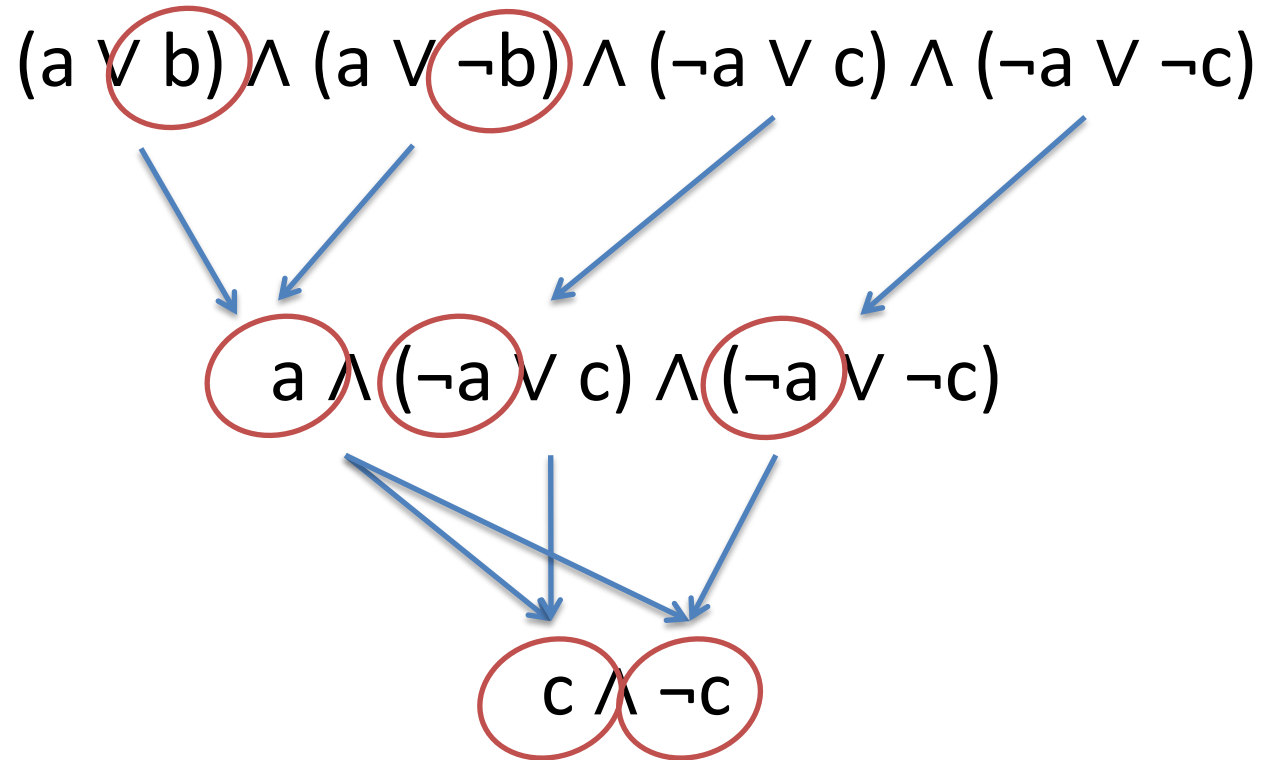
Resolution



Resolution



Resolution



(Part of) Davis Putnam Algorithm

- (Also: when a variable appears in only one polarity, remove all clauses containing it.)
- M. Davis, H. Putnam, *A computing procedure for quantification theory*, JACM, 1960.
- Problem: space explosion!
- DP is *proof-oriented*. Current algorithms are *model-oriented*.

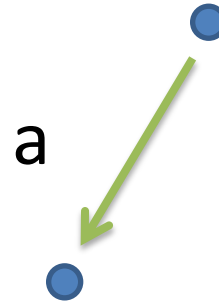
Backtracking Search

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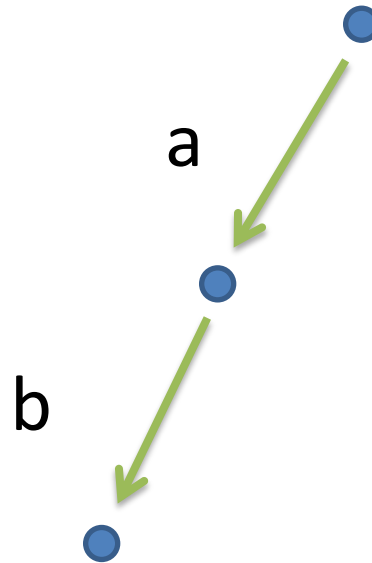
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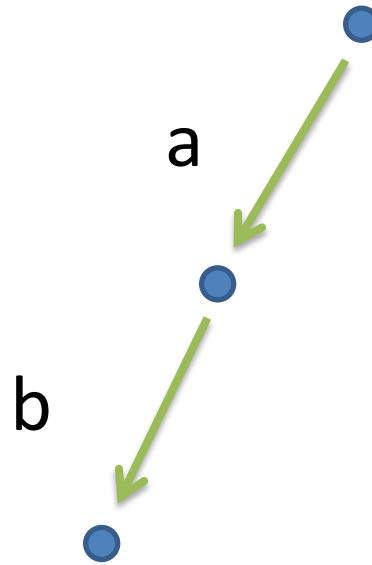
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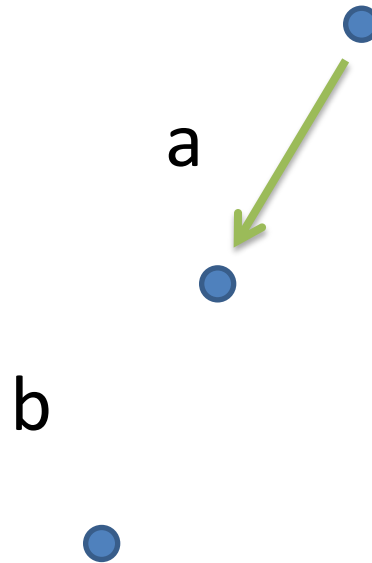
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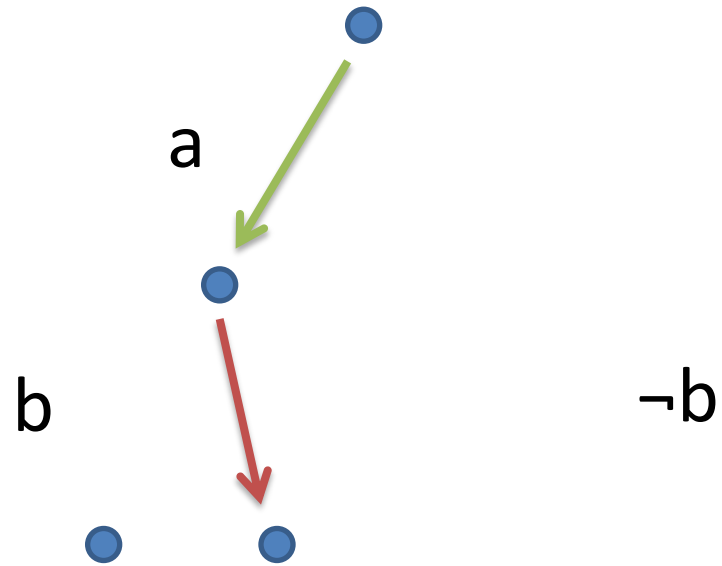
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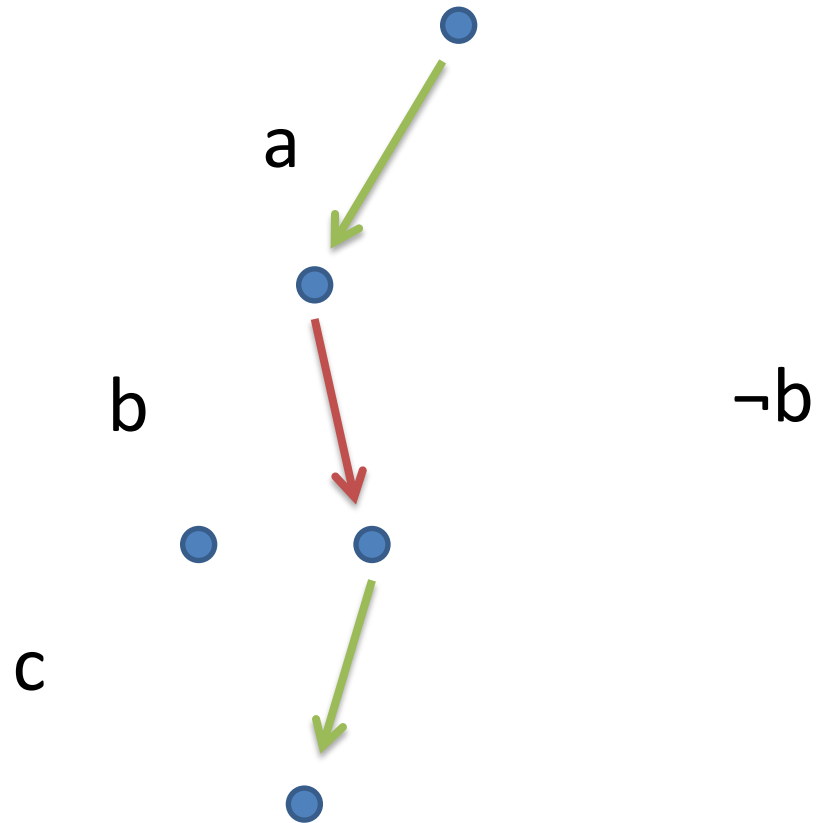
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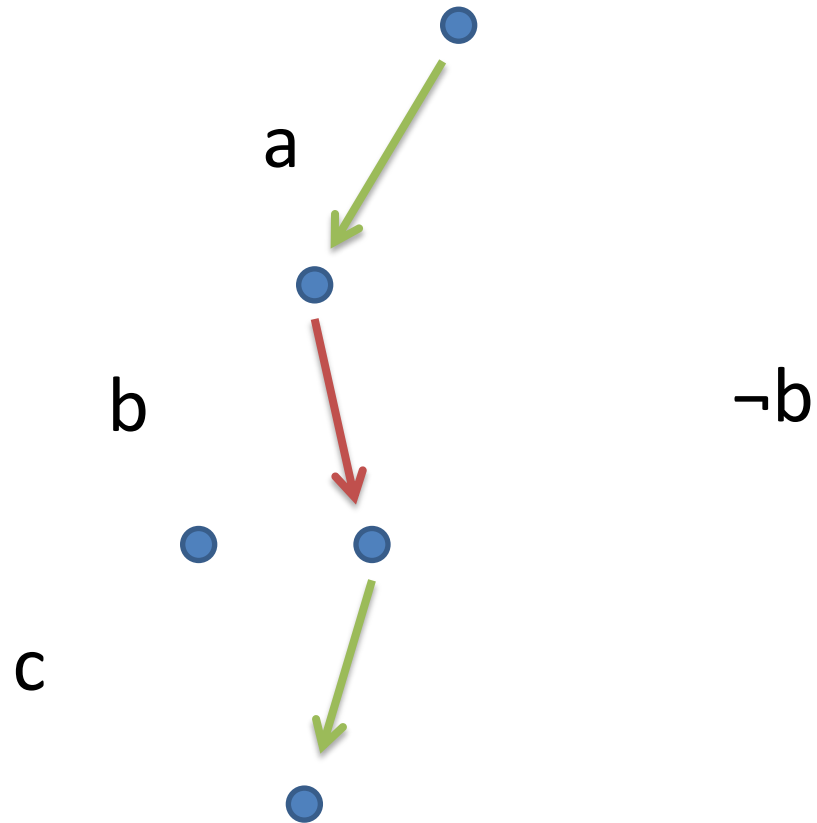
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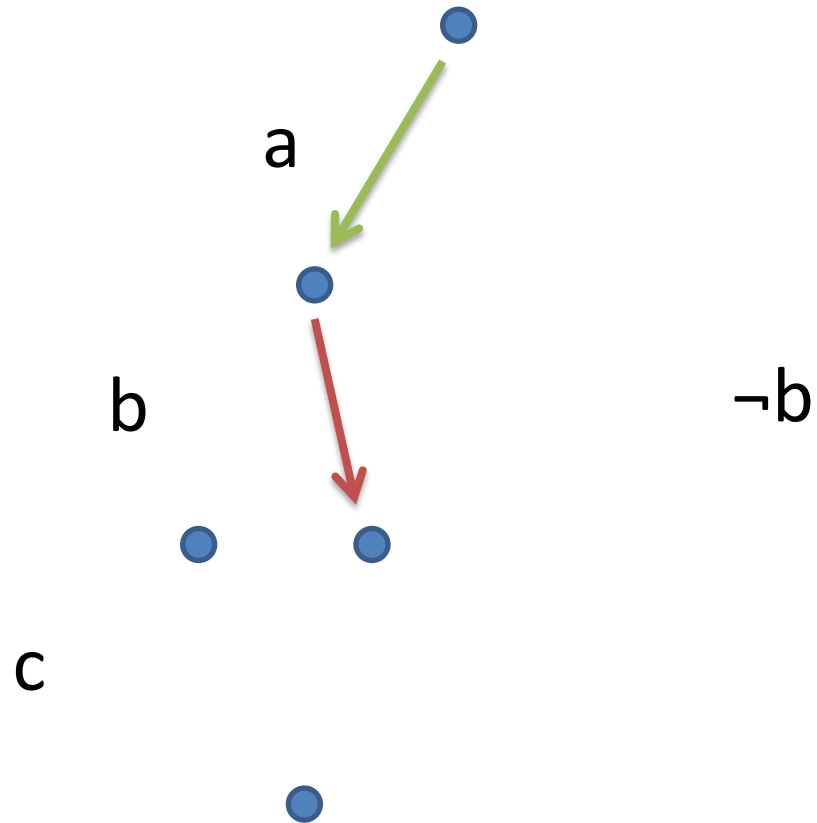
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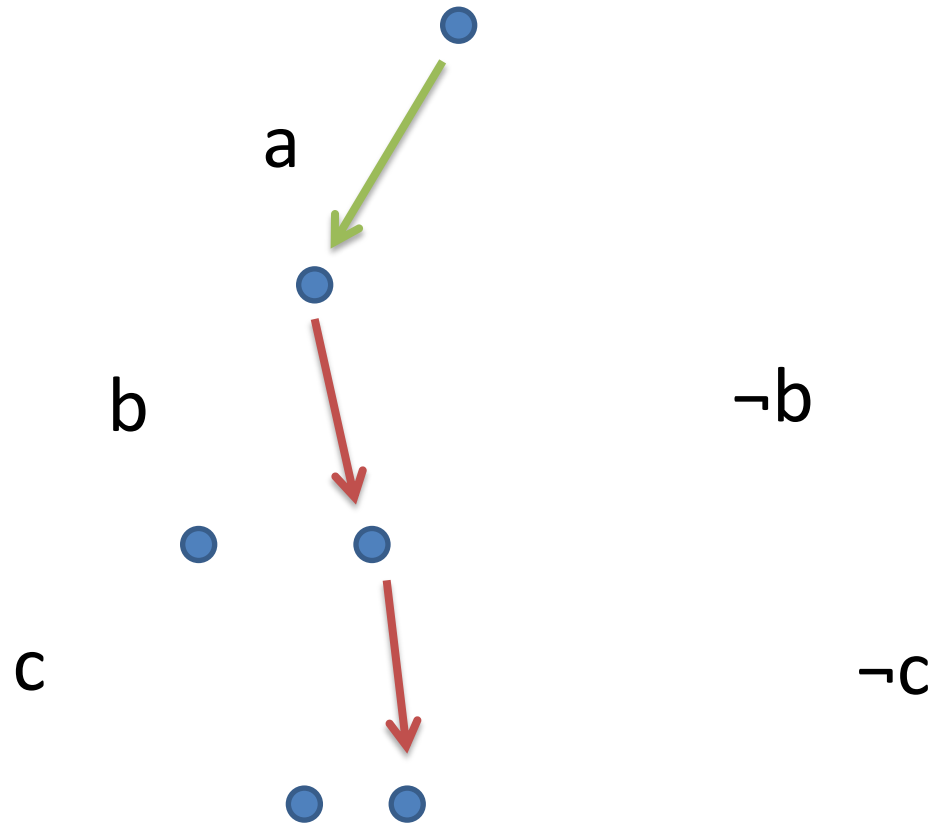
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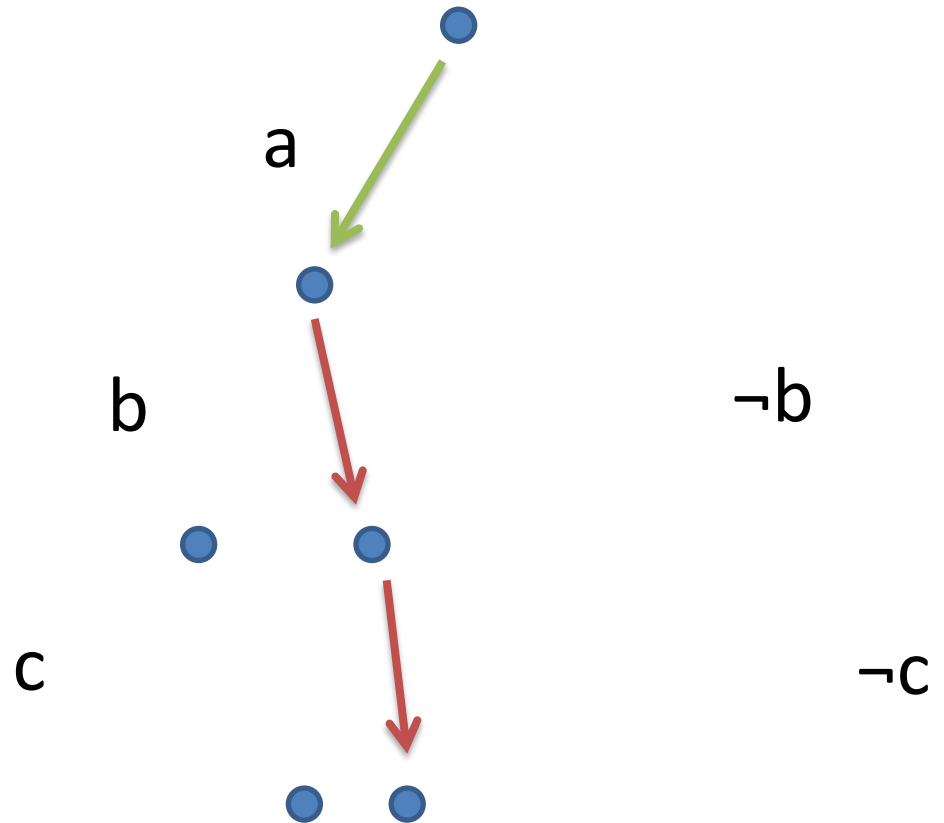
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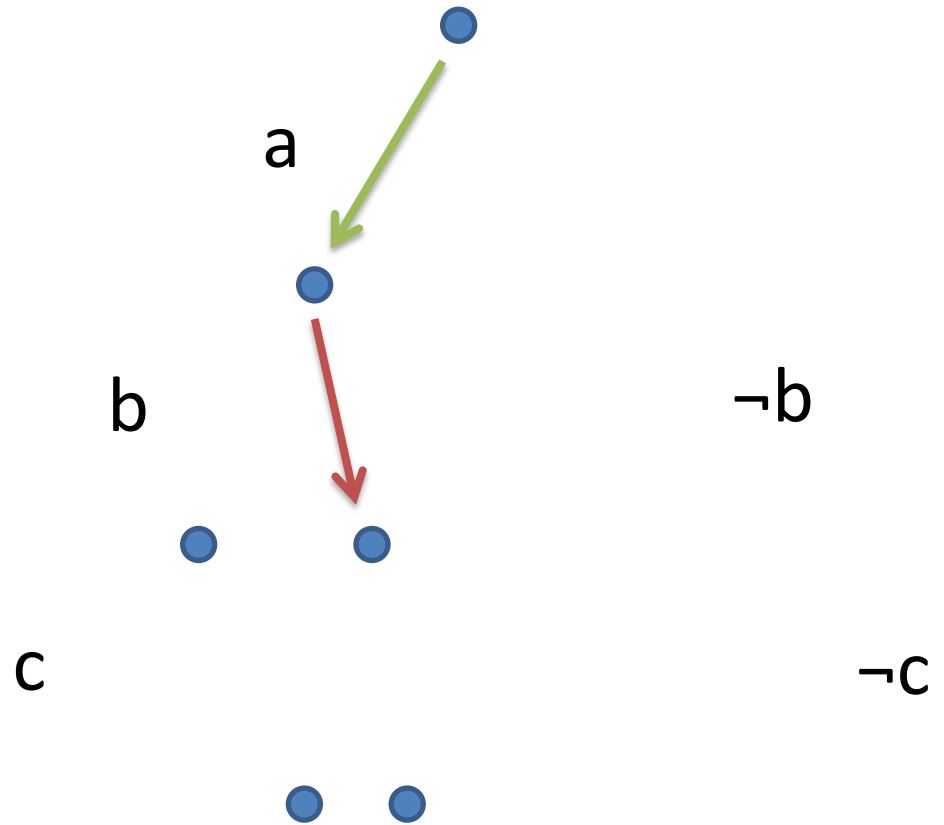
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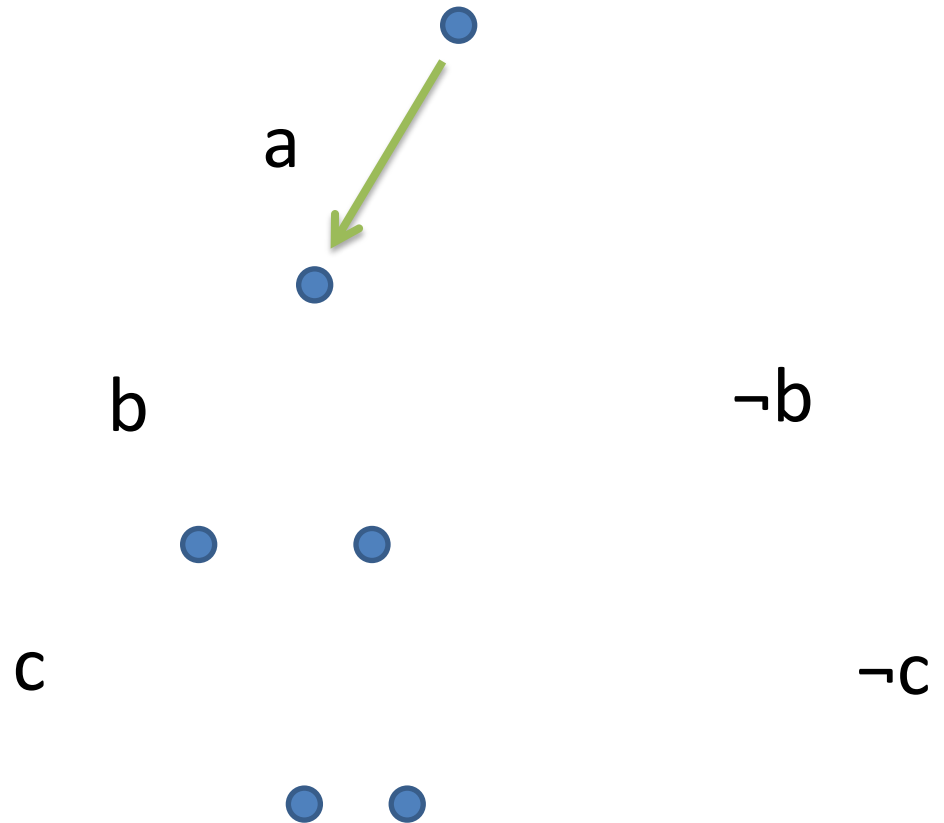
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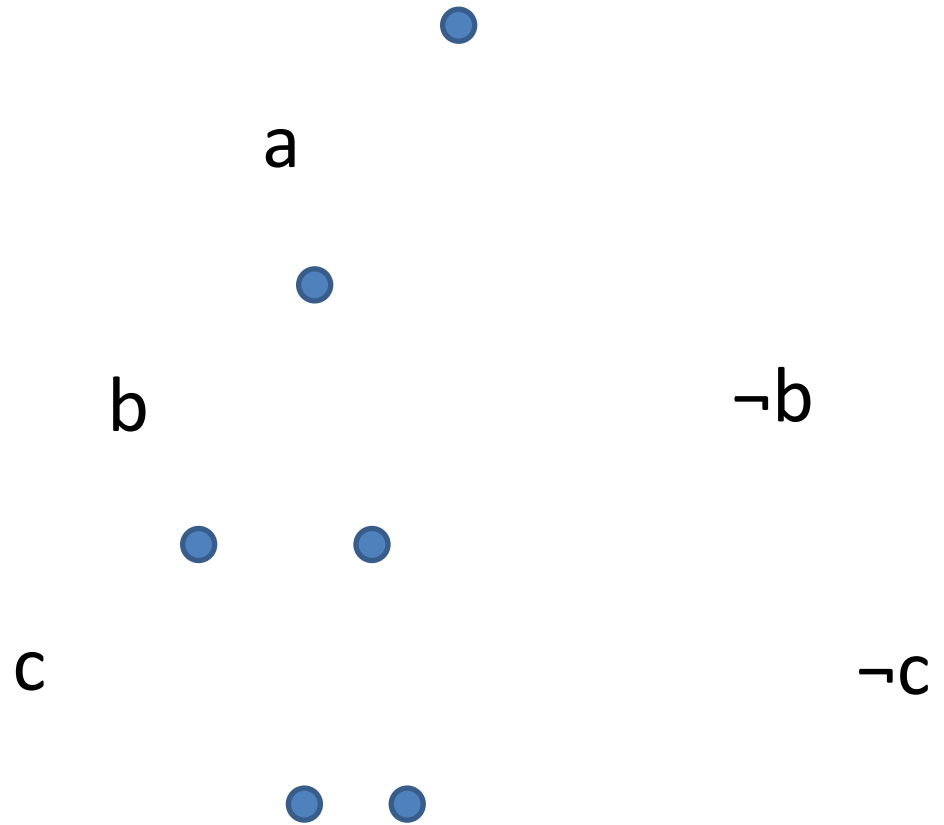
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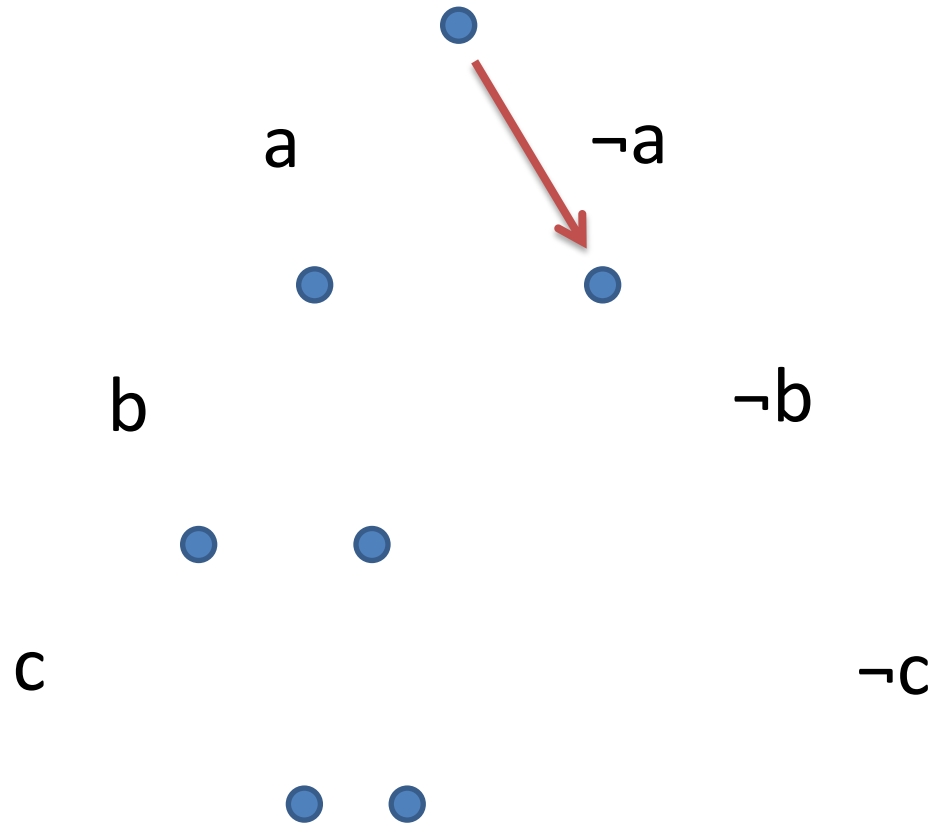
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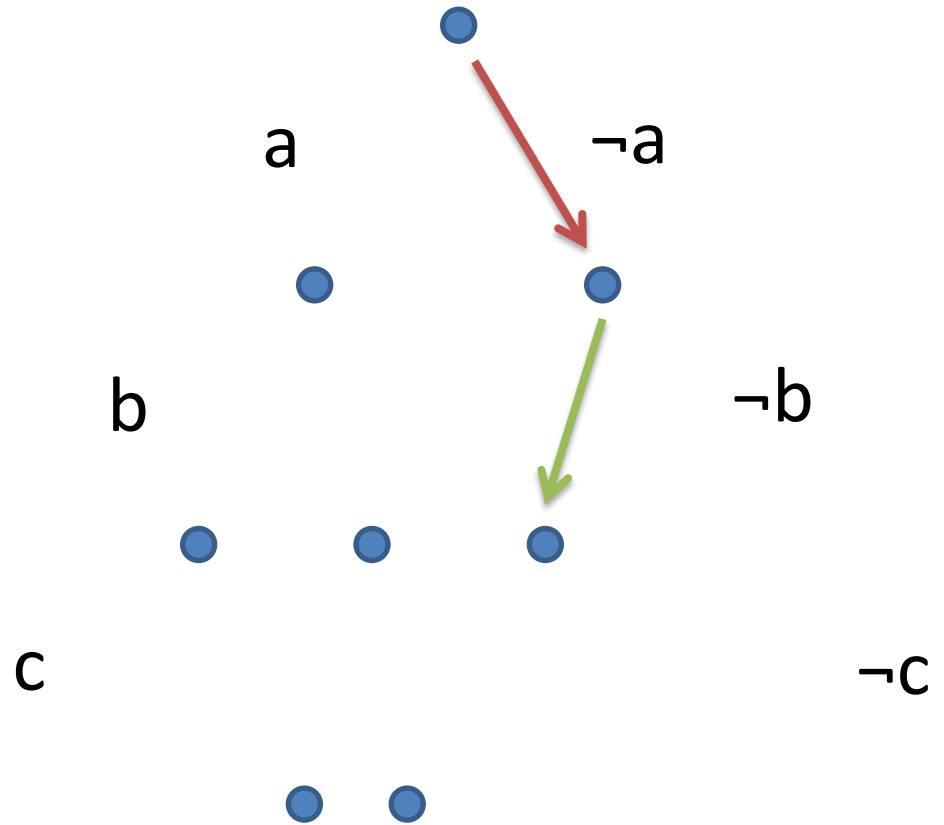
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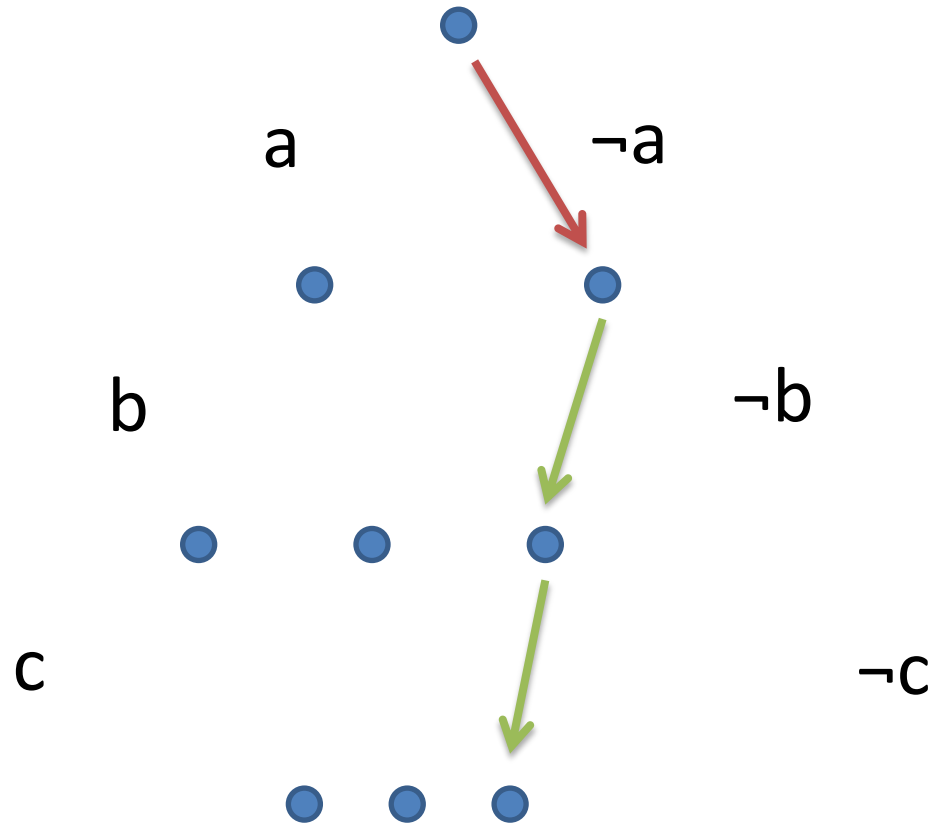
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Boolean Constraint Propagation

- *“When all but one literal are falsified, it becomes implied.”*

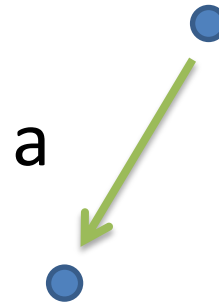
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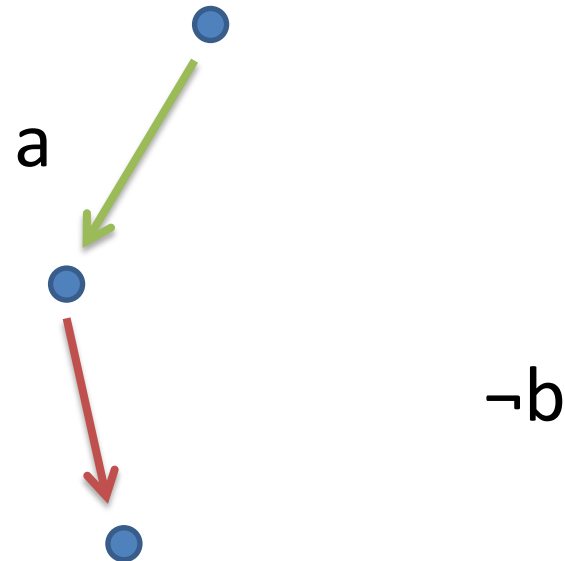
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- *“When all but one literal are falsified, it becomes implied.”*

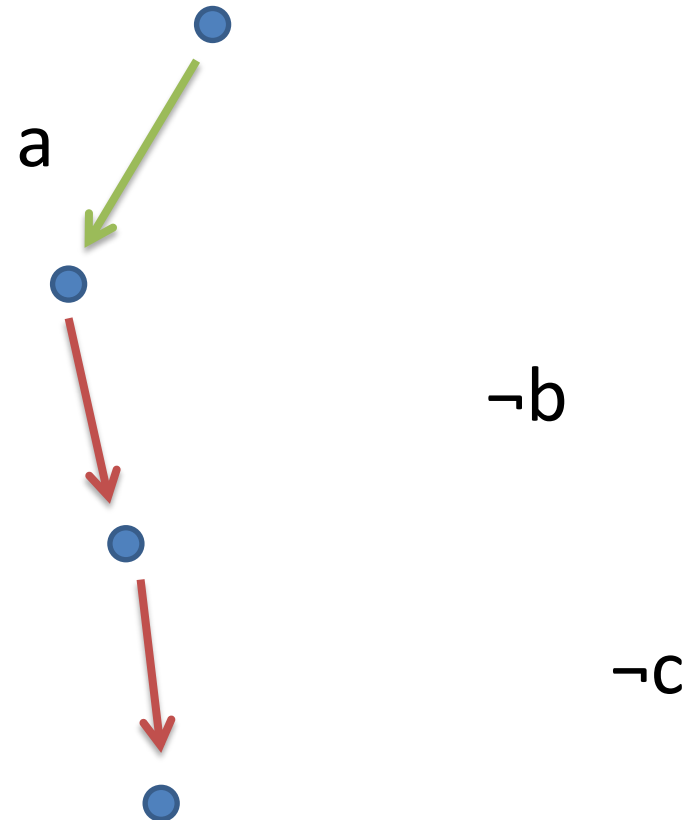
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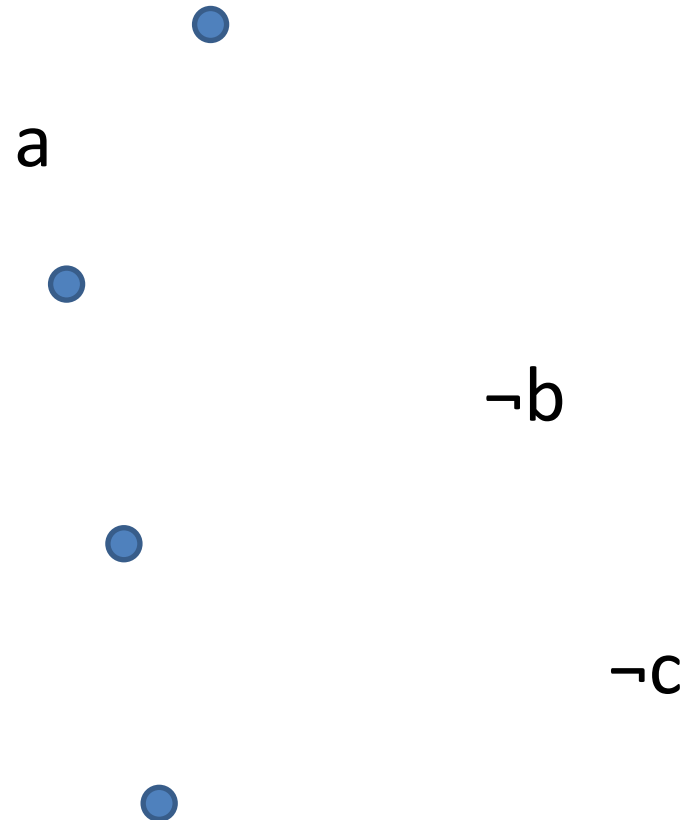
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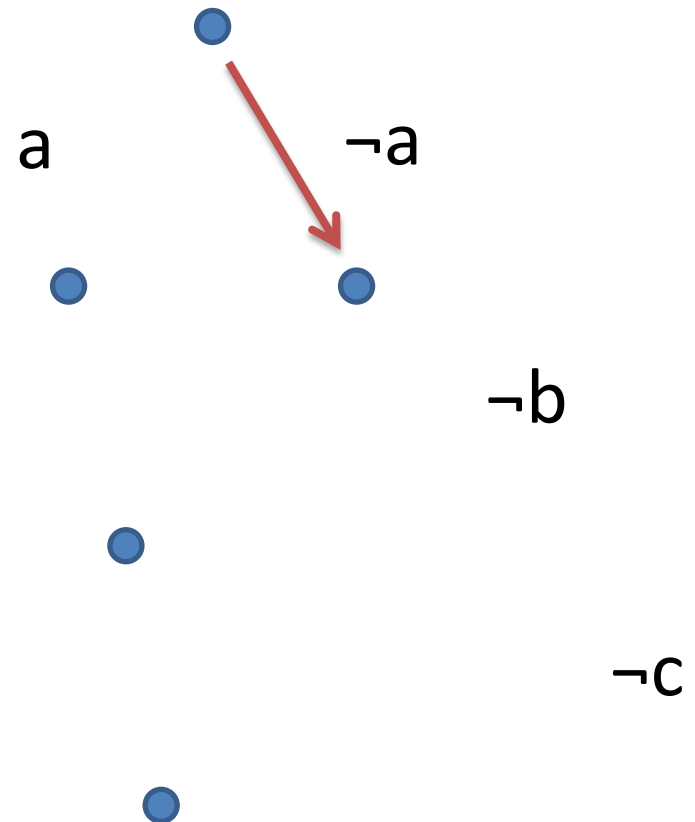
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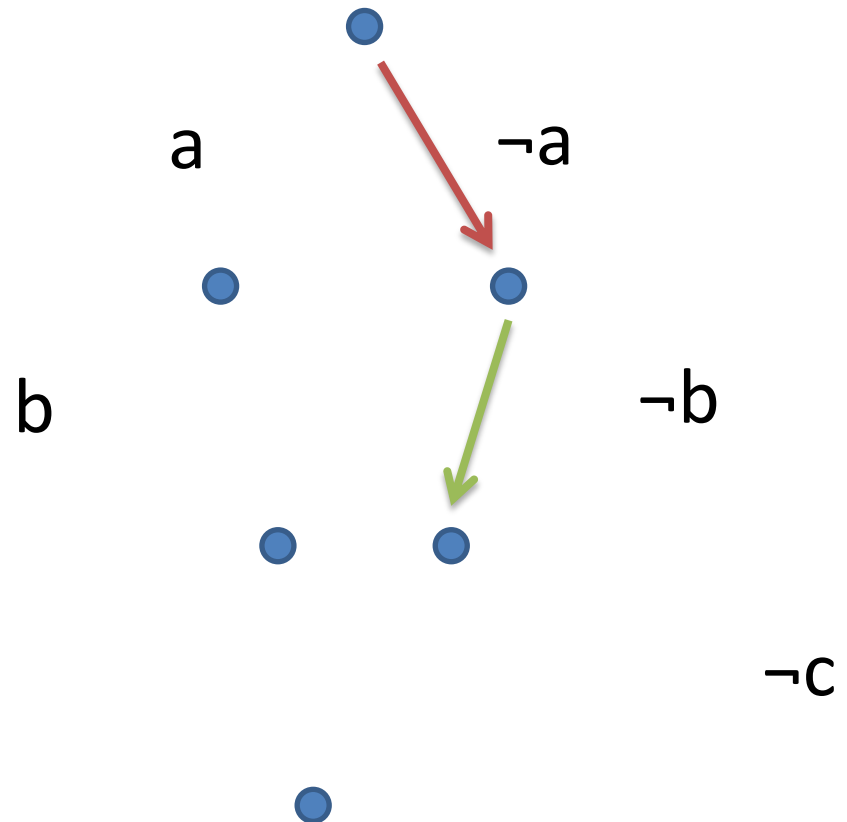
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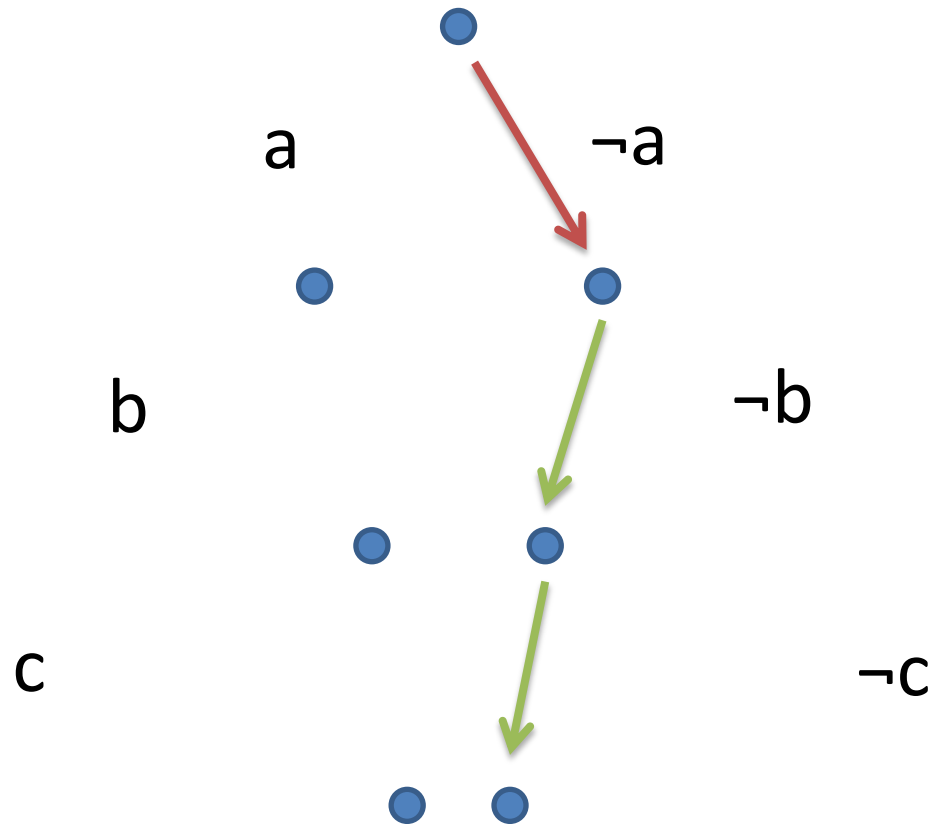
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Two-watched-literal Scheme for BCP

- BCP can cut the search tree dramatically...
- ...but checking each clause for potential implications is expensive.
- Observation: as long as at least two literals in a clause are “not false”, that clause does not imply any new literal.
- Idea: for each clause, try to maintain that invariant.

Cutting Deeper: Learning

- Idea: compute new clauses that are logically implied, and that may trigger more BCP.
- Use an *implication graph*. When a conflict is derived, look for a *small explanation*.

Learning

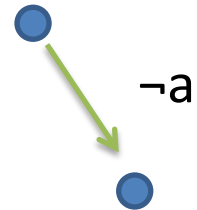


(a \vee d)
 \wedge (a \vee \neg c \vee \neg h)
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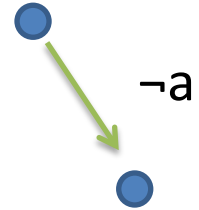
\neg a



Learning

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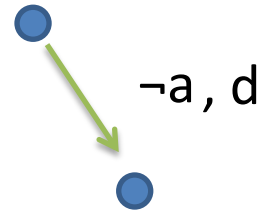
$\neg a$



Learning

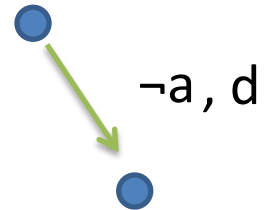
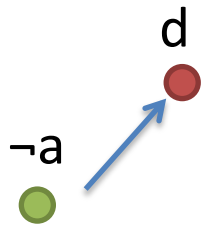
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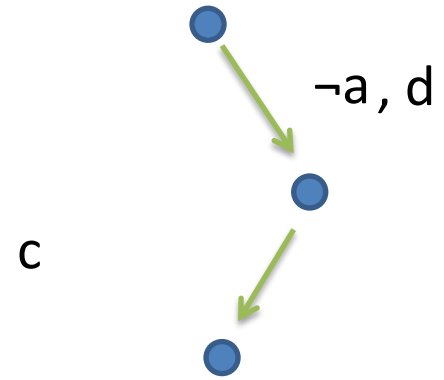
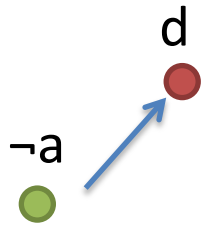
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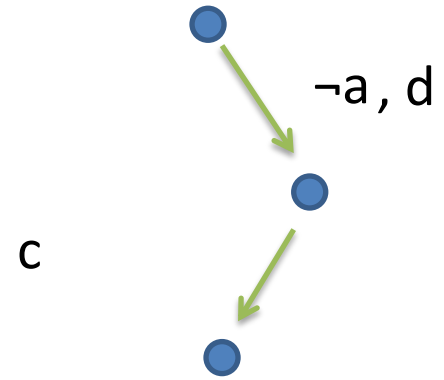
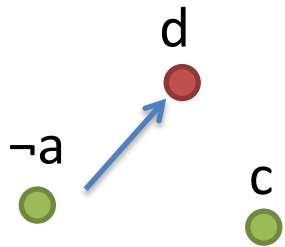
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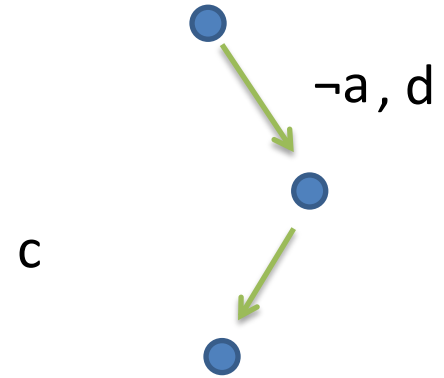
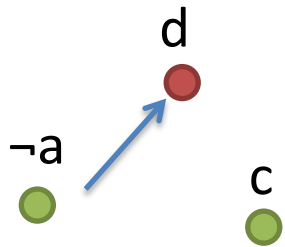
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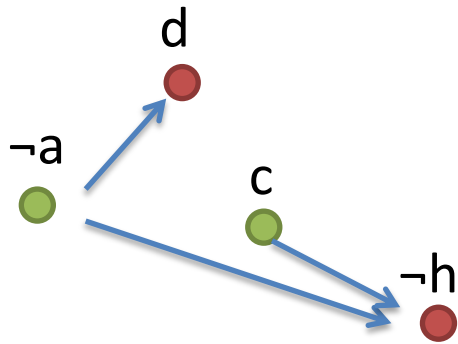
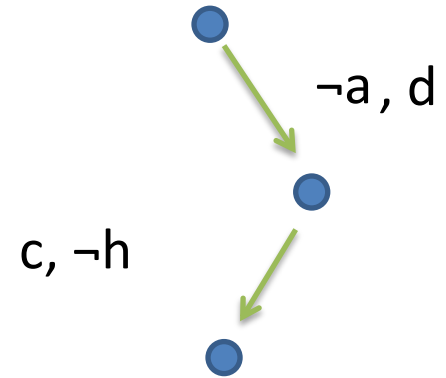
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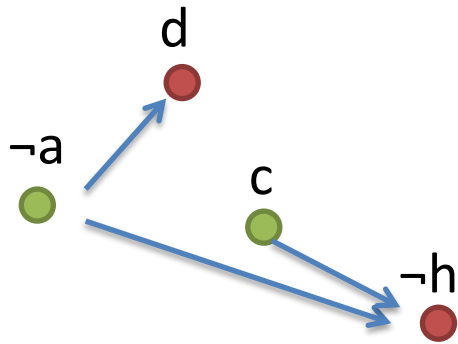
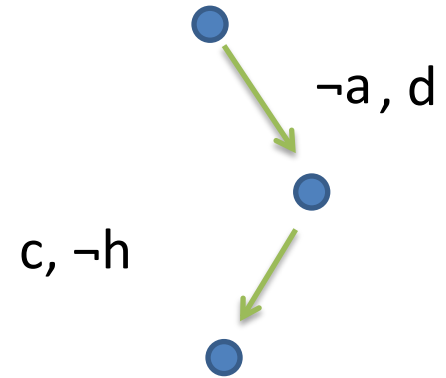
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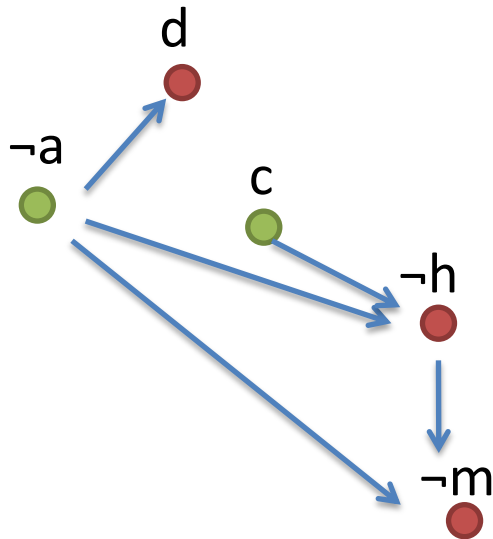
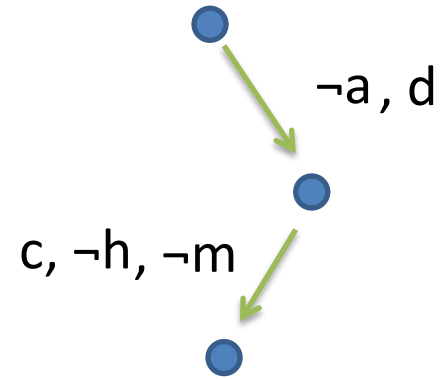
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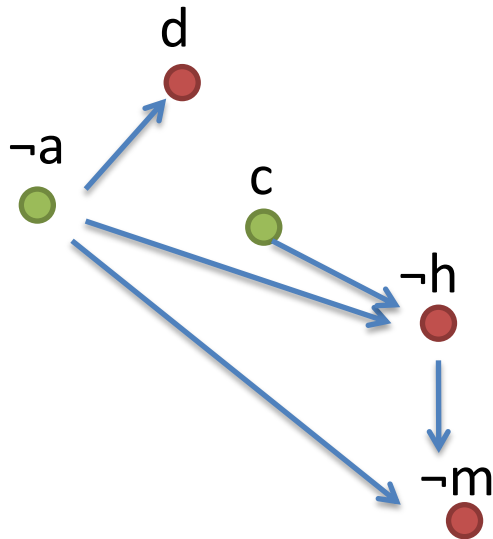
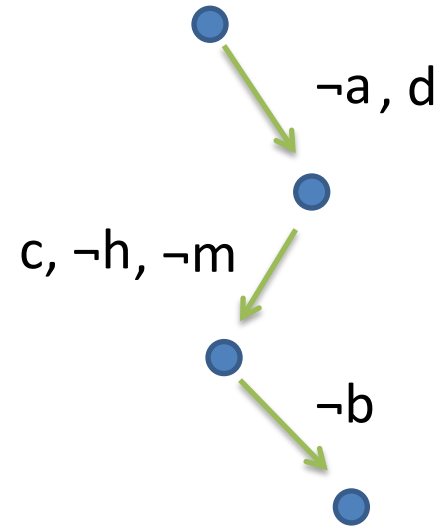
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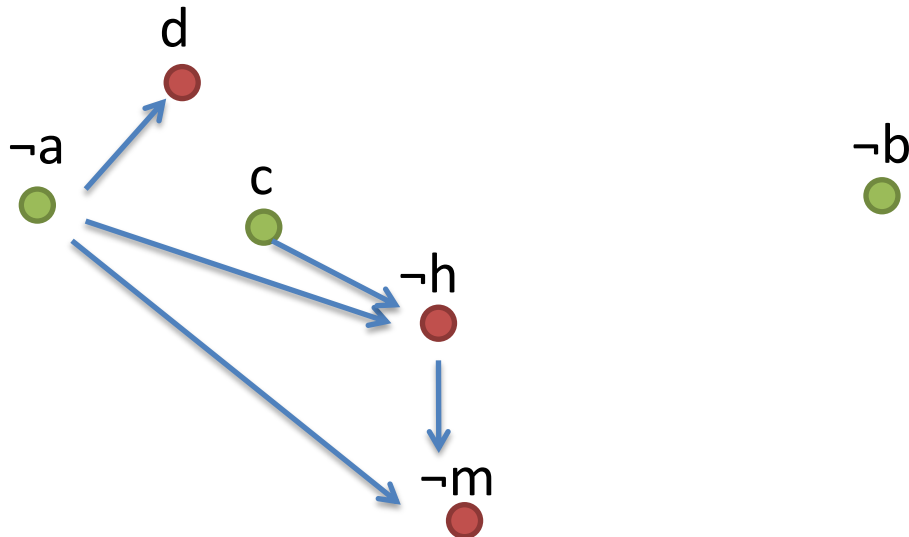
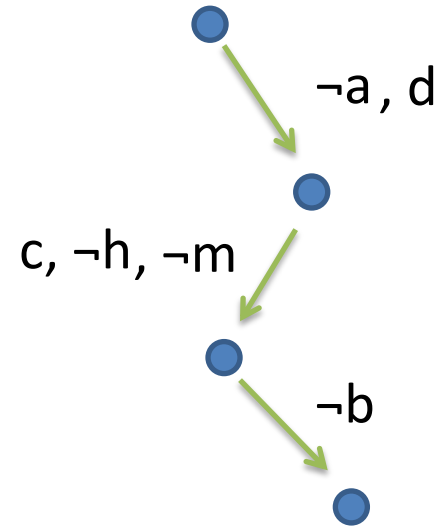
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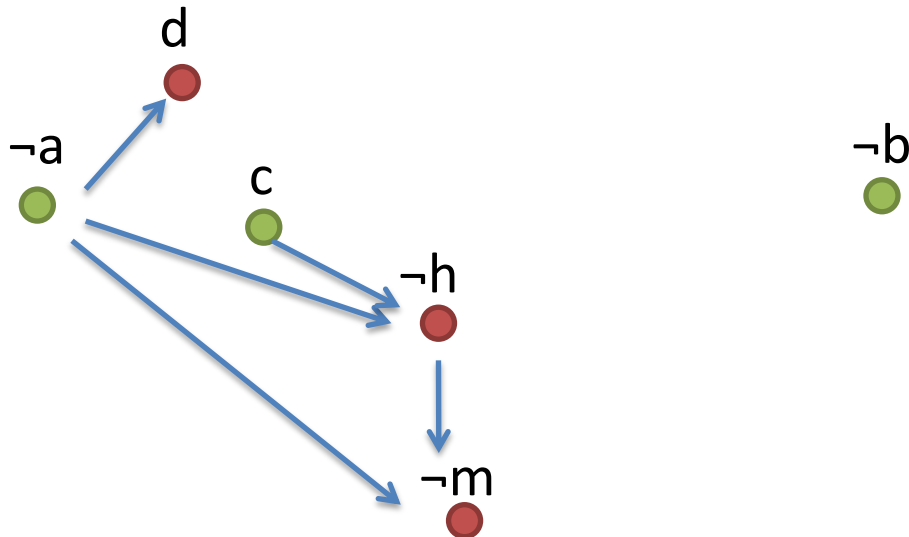
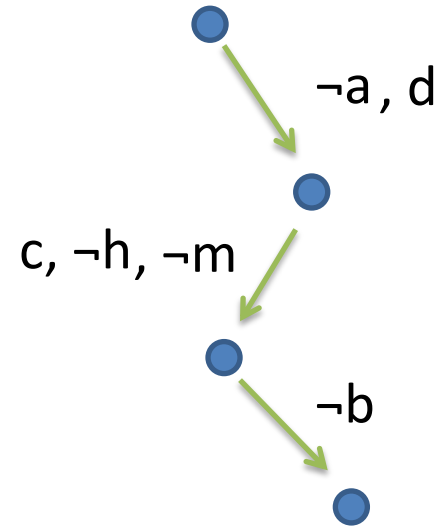
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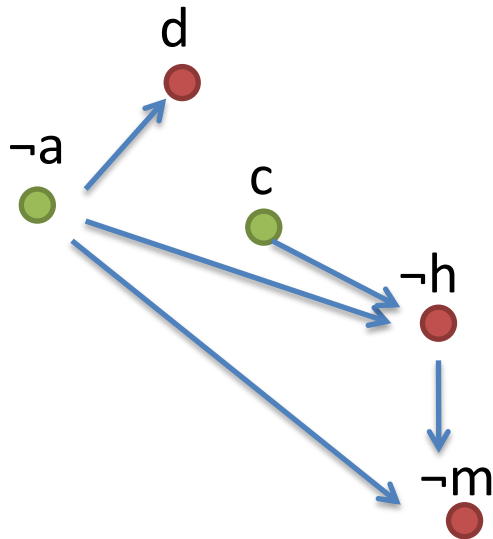
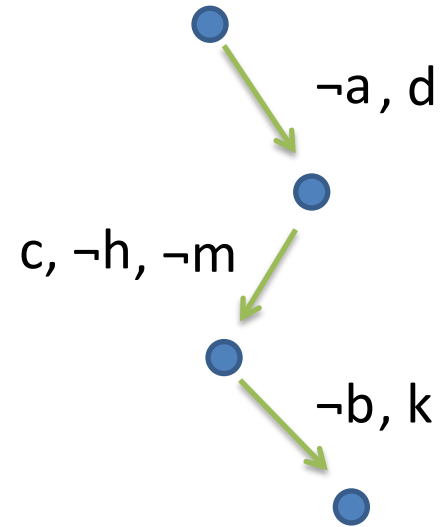
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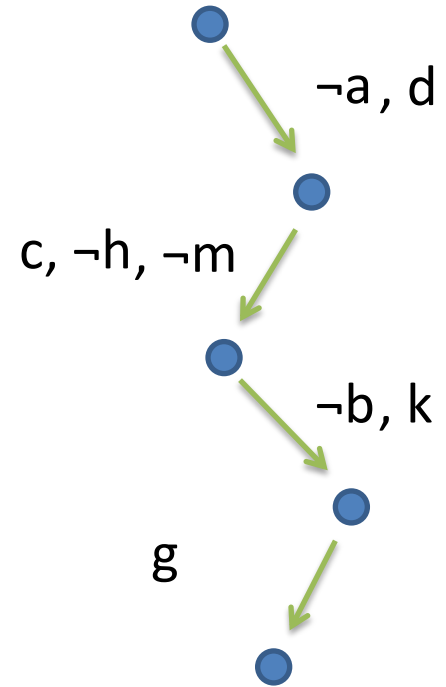
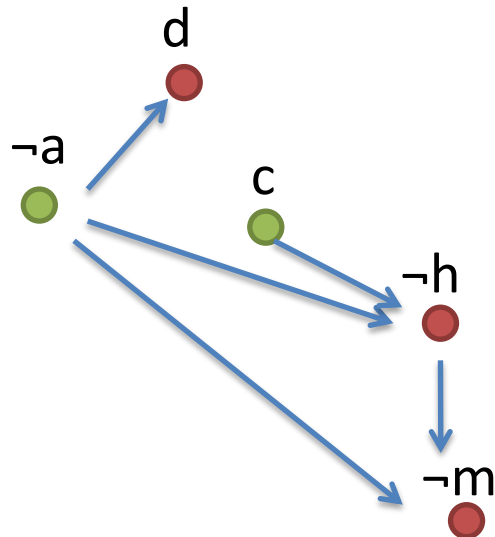
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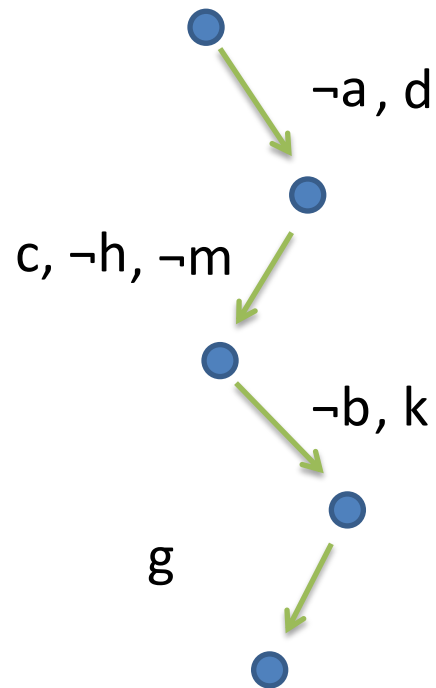
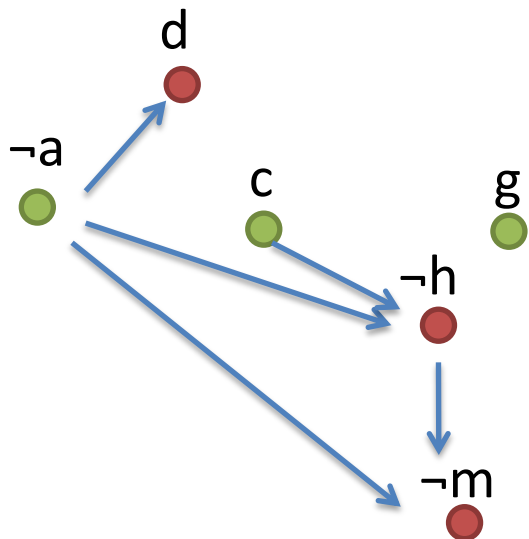
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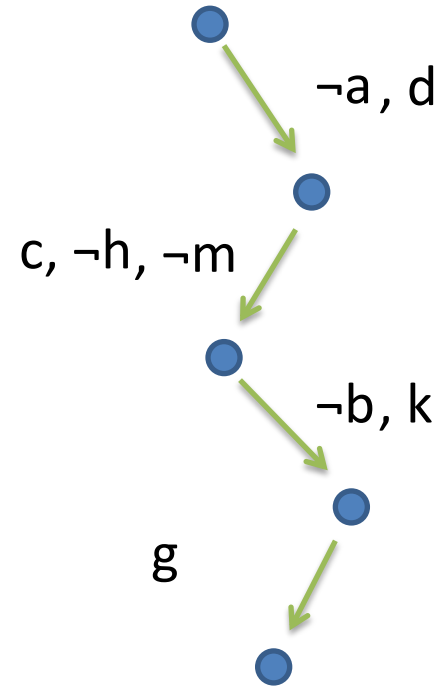
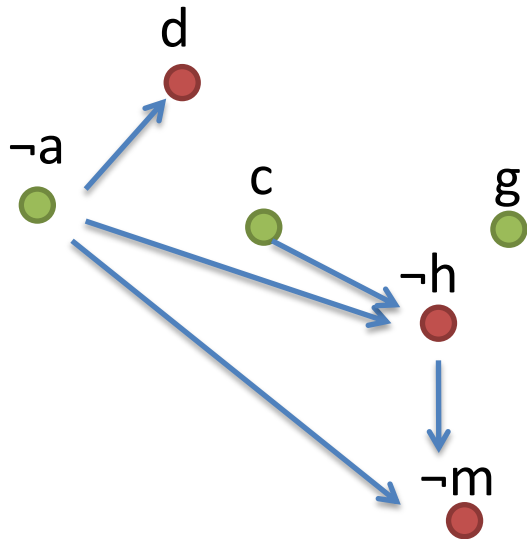
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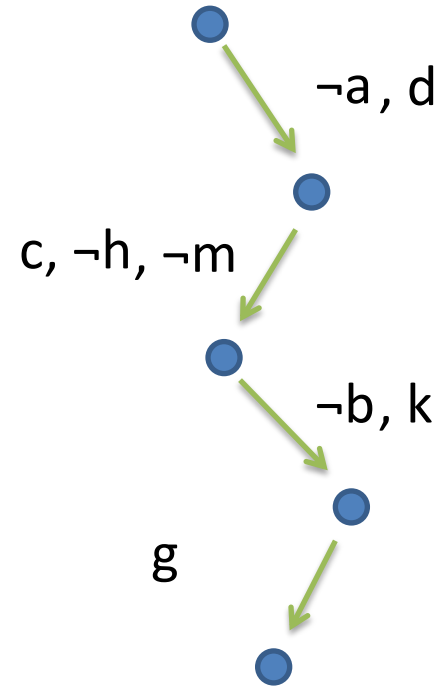
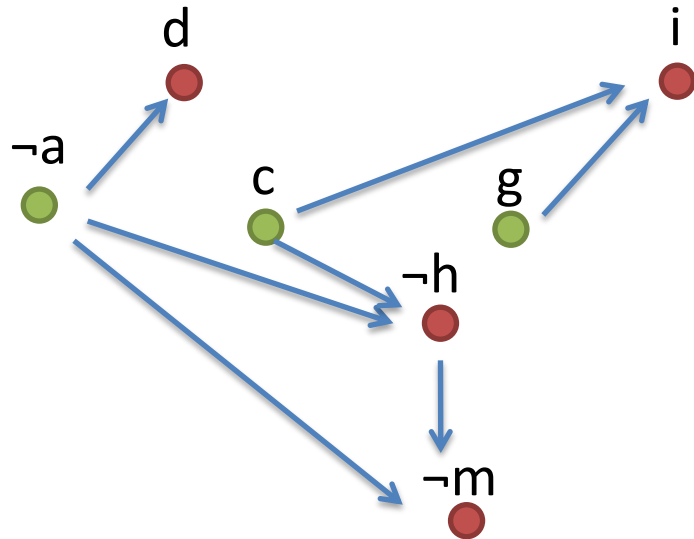
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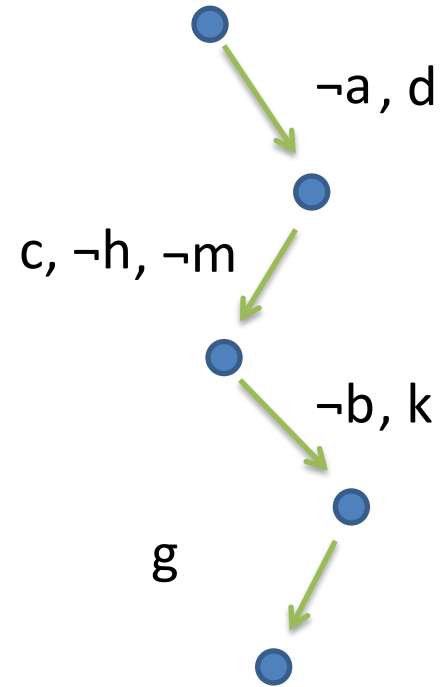
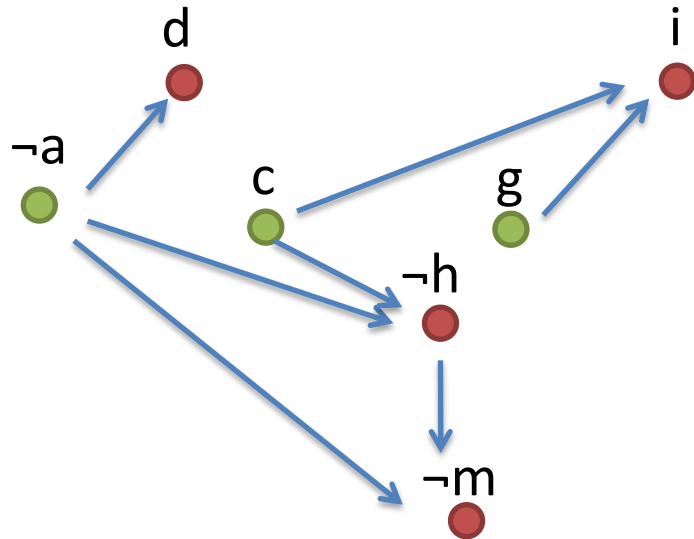
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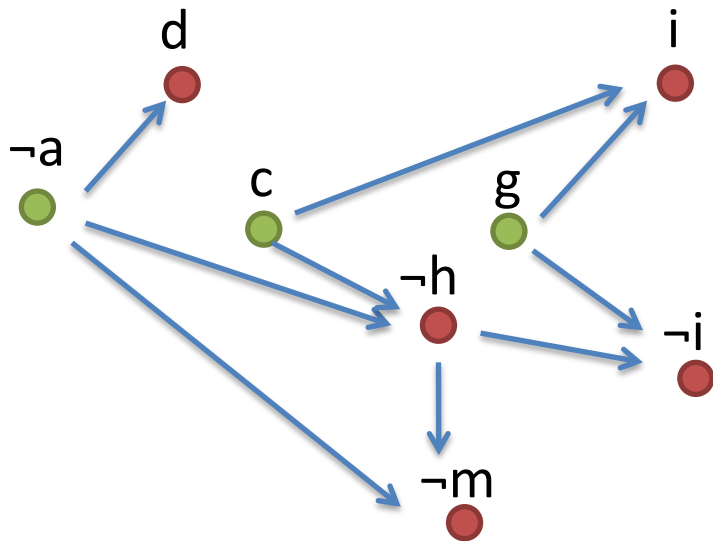
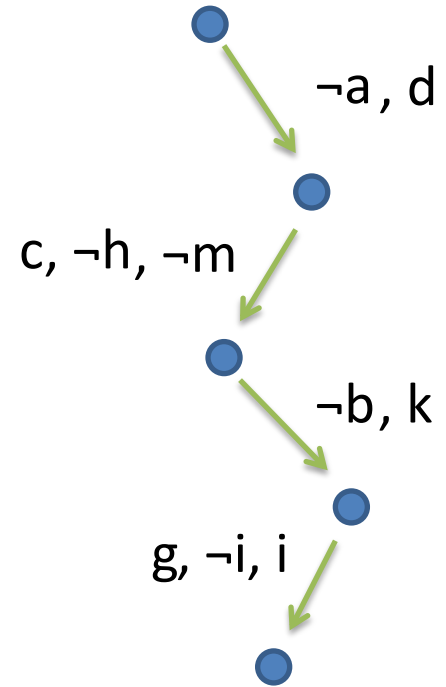
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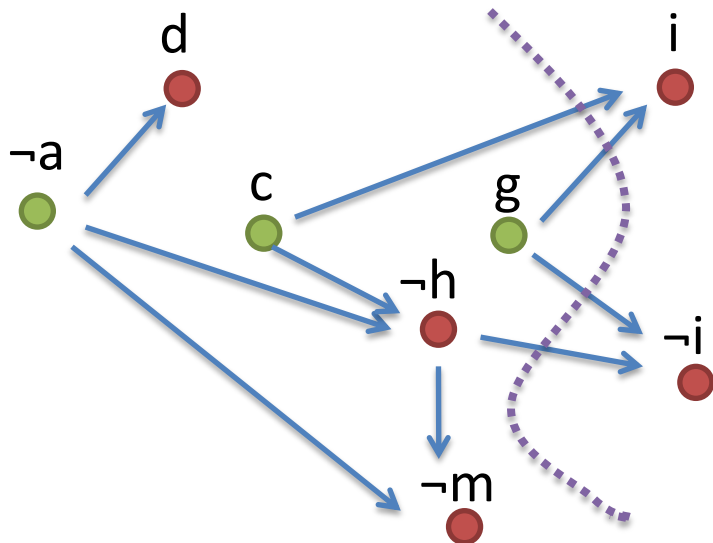
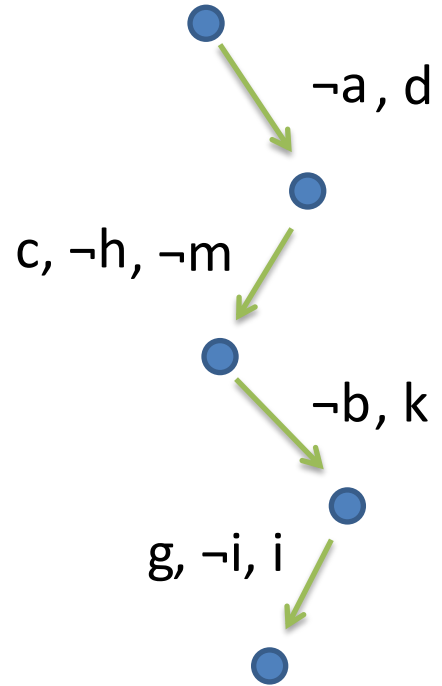
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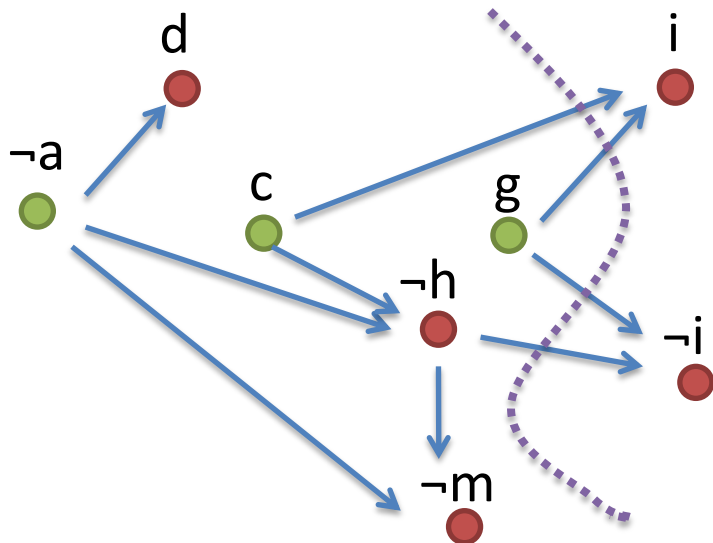
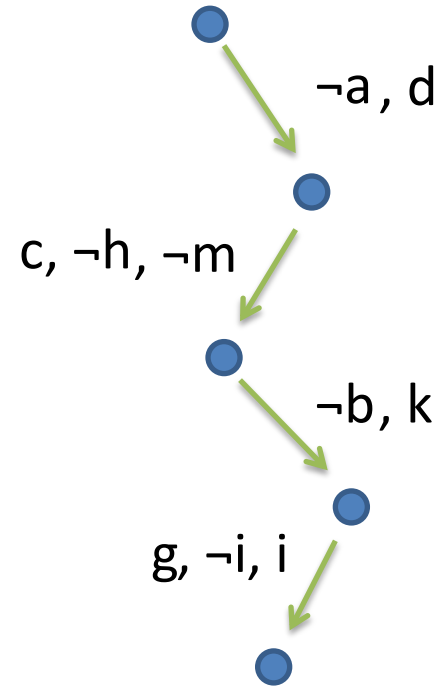
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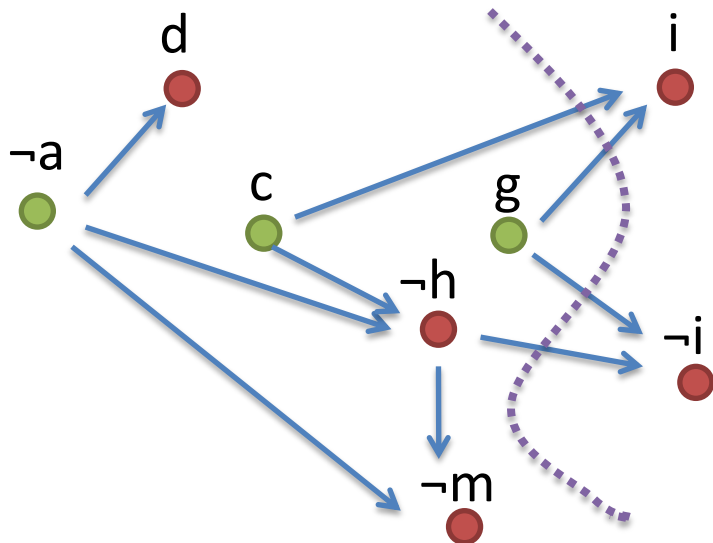
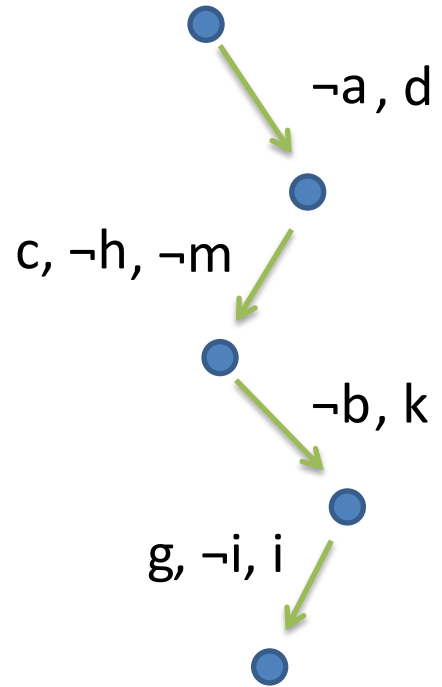
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$\neg(c \wedge g \wedge \neg h)$

Learning

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$\neg(c \wedge g \wedge \neg h)$

...and backtrack to c, then assert \neg g !

Learning

- Learning has a dramatically positive impact.
- Learning also makes *restarts* possible:
 - Idea: after some number of literal assignments, drop the assignment stack and restart from zero.
 - Goal: avoid locally difficult subtrees.
 - Clauses encode previous knowledge and make new search faster.

Picking Variable Assignments

- Potential strategies:
 - Fixed ordering,
 - Frequency based,
 - “Maximal impact”.

Picking Variable Assignments

- Potential strategies:
 - Fixed ordering,
 - Frequency based,
 - “Maximal impact”.
- Overall favorite are activity-based heuristics:
 - Pick variables that you have seen a lot in conflicts.
 - Decay weights to favor recent conflicts.
 - Cheap to compute/update.

More Engineering...

- SAT dirty little secret: the enormous impact of preprocessing.
 - Problems are generated automatically (“compiled”); many redundancies, symmetry, etc.
 - Preprocessors look for subsumed clauses, equivalent clauses, etc.
 - Typically, run with timeout, then DPLL search.

More Engineering...

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 - Typically, run with timeout, then DPLL search.
- Parallel SAT
 - State-of-the-art is to run instances with different parameters in parallel.

Beyond SAT

- SMT solvers
 - Idea: use a SAT solver for the propositional structure, and theory solvers for conjunction of literals.
- QBF
 - SAT with quantifiers. PSPACE complete.