Range Analysis Domain for BigInt

Domain values D include:

- bounded intervals of integers: [a,b]
- intervals unbounded from one side (-∞,b], [a,∞)
- empty set, denoted ⊥
- the set of all integers, denoted T

Formally, if **Z** denotes integers, then

D = {
$$\bot$$
,T} U { [a,b] | a,b∈**Z**}
U {(-∞,b] | b∈**Z**}
U {[a,∞) | a∈**Z**}

D is an infinite set!

Sequences in Analysis are Monotonically Growing

Transfer functions (describe how statements affect elements of D) should be monotonic:

if we start with a representation of a larger set of states, the representation of the resulting set of states should also be larger

x: [a,b]
$$\bigvee_{2}$$
, x = x + 2 \bigvee_{2} , x: [a+2,b+2]

$$d_1 \le d_2 \text{ implies } [[x=x+2]](d_1) \le [[x=x+2]](d_2)$$

We start from \bot everywhere except entry So in first step, the values can only grow $\bot \le d_2$ implies $[[x=x+2]](\bot) \le [[x=x+2]](d_2)$ Values computed in second step are also bigger If $x_n = F^n(\bot)$ then $x_0 = \bot \le x_1$ $x_n \le x_{n+1} / F$ $F(x_n) \le F(x_{n+1})$ i.e. $x_{n+1} \le x_{n+2}$

How Long Does Analysis Take?

- We explore this question by comparing
 - range analysis: maintain intervals
 - constant propagation:
 maintains indication whether the value is constant

Iterating Range Analysis

Find the **number** of updates **range analysis** needs to **stabilize** in the following code

```
x = 1
n = 1000
while (x < n) {
  x = x + 2
}</pre>
```

Iterating Range Analysis

Now, if we assume that any number can be entered from the user, what is now the number of steps?

One solution: "smaller" domain

Range Analysis with Finite Set of Endpoints

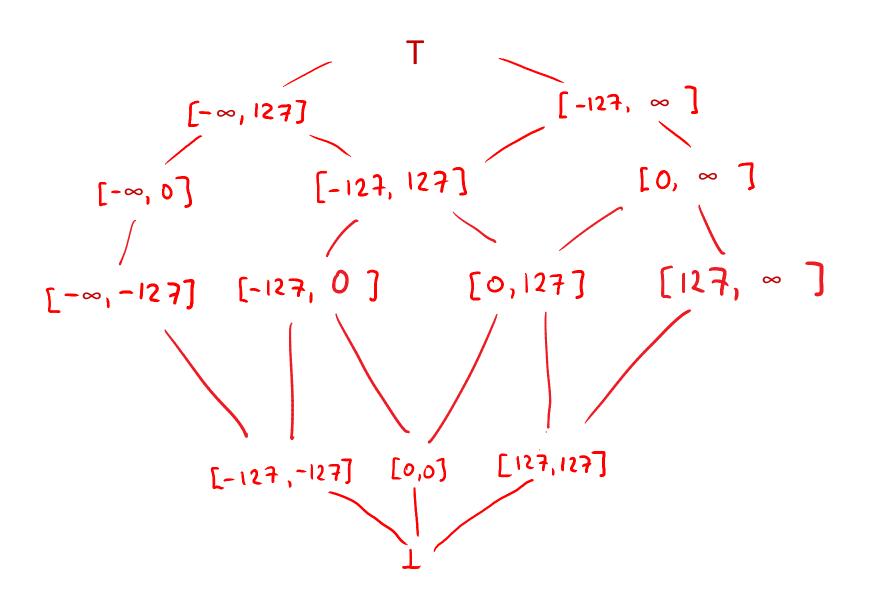
Pick a set W of "interesting" interval end-points Example:

$$W = \{-128, 0, 127\}$$

D = {
$$\bot$$
,T} U { [a,b] | a,b \in W, a \leq b}
U {(- ∞ ,b] | b \in W}
U {[a, ∞) | a \in W}

D is a finite set! How many elements does it have?

Domain Lattice Diagram for this W: 14 elements



Re-Run Analysis with Finite Endpoint Set

What is the number of updates?

```
x = 1 x = 1

n = 1000 n = readInt()

while (x < n) \{ x = x + 2 \} \{ x = x + 2 \}
```

Constant Propagation Domain

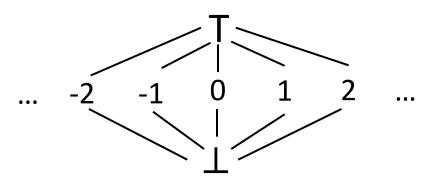
Domain values D are:

- intervals [a,a], denoted simply 'a'
- empty set, denoted ⊥ and set of all integers T

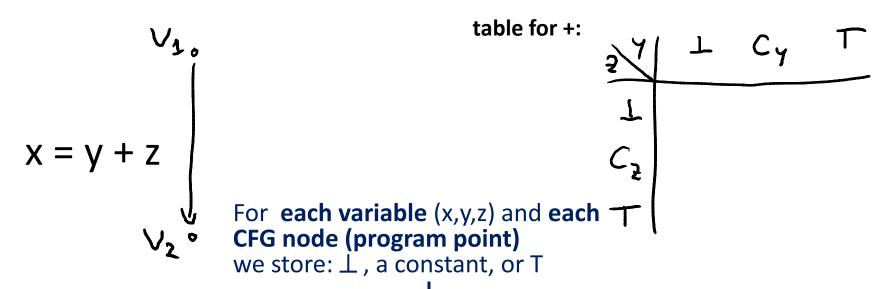
Formally, if **Z** denotes integers, then

$$D = \{\bot,T\} \cup \{a \mid a \in Z\}$$

D is an infinite set



Constant Propagation Transfer Functions



```
abstract class Flement
                                                    def tableForPlus(y:Element, z:Element) =
case class Top extends Element
                                                     (x,y) match {
case class Bot extends Element
                                                    case (Const(cy),Const(cz)) => Const(cy+cz)
case class Const(v:Int) extends Element
                                                    case (Bot, ) => Bot
var facts: Map[Nodes, Map[VarNames, Element]]
                                                    case ( ,Bot) => Bot
 what executes during analysis of x=y+z:
                                                     case (Top,Const(cz)) => Top
oldY = facts(v_1)("y")
                                                     case (Const(cy),Top) => Top
oldZ = facts(v_1)("z")
newX = tableForPlus(oldY, oldZ)
facts(v_2) = facts(v_2) join facts(v_1).updated("x", newX)
```

Run Constant Propagation

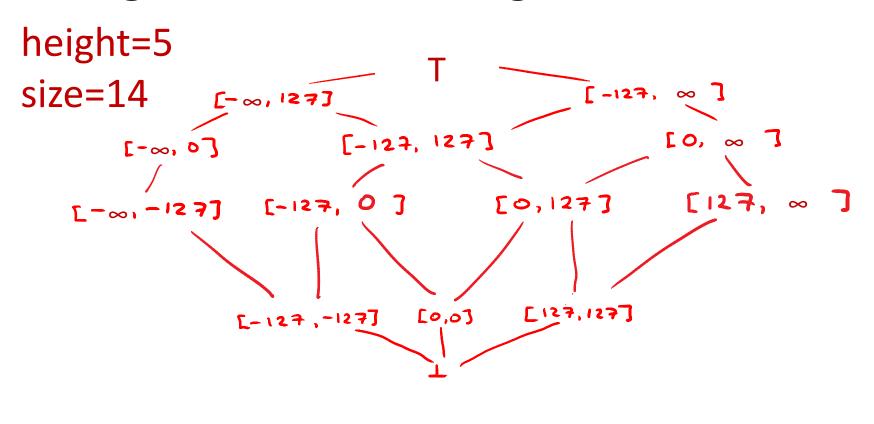
What is the number of updates?

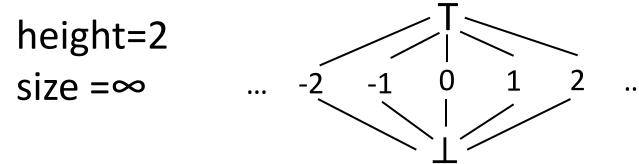
```
x = 1 x = 1 n = 1000 n = readInt() while (x < n) { x = x + 2 x = x + 2 }
```

Observe

- Range analysis with W = {-128, 0, 127} has a finite domain
- Constant propagation has infinite domain (for every integer constant, one element)
- Yet, constant propagation finishes sooner!
 - it is not about the size of the domain
 - it is about the height

Height of Lattice: Length of Max. Chain

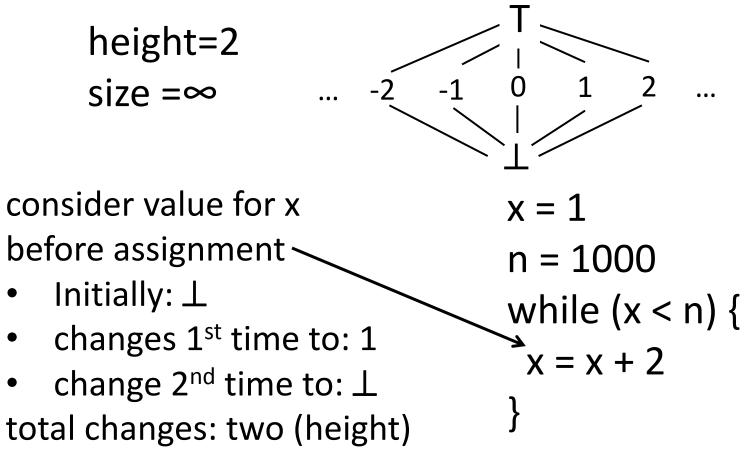




Chain of Length n

- A set of elements x₀,x₁,..., x_n in D that are linearly ordered, that is x₀ < x₁ < ... < x_n
- A lattice can have many chains. Its height is the maximum n for all the chains
- If there is no upper bound on lengths of chains, we say lattice has infinite height
- Any monotonic sequence of distinct elements has length at most equal to lattice height
 - including sequence occuring during analysis!
 - such sequences are always monotonic

In constant propagation, each value can change only twice



var facts : Map[Nodes,Map[VarNames,Element]]

Total number of changes bounded by: height · | Nodes | · | Vars |

Exercise

- B_{32} the set of all 32-bit integers What is the upper bound for number of changes in the entire analysis for:
 - 3 variables,
 - 7 program points

for these two analyses:

- 1) constant propagation for constants from B_{32}
- 2) The following domain D:

$$D = \{\bot\} \cup \{ [a,b] \mid a,b \in B_{32}, a \le b \}$$

Height of B₃₂

```
D = \{\bot\} U \{[a,b] \mid a,b \in \mathbf{B_{32}}, a \le b\}
One possible chain of maximal length:
\bot
```

• • •

[MinInt, MaxInt]

Initialization Analysis

first initialization

write use use write use uninitialized

What does javac say to this:

```
class Test {
  static void test(int p) {
        int n;
         p = p - 1;
        if (p > 0) {
           n = 100;
        while (n != 0) {
           System.out.println(n);
           n = n - p;
                       Test.java:8: variable n might not have been initialized
                               while (n > 0) {
                                Λ
                       1 error
```

Program that compiles in java

```
class Test {
  static void test(int p) {
         int n;
         p = p - 1;
         if (p > 0) {
            n = 100;
         else {
            n = -100;
         while (n != 0) {
            System.out.println(n);
            n = n - p;
```

We would like variables to be initialized on all execution paths.

Otherwise, the program execution could be undesirable affected by the value that was in the variable initially.

We can enforce such check using initialization analysis.

What does javac say to this?

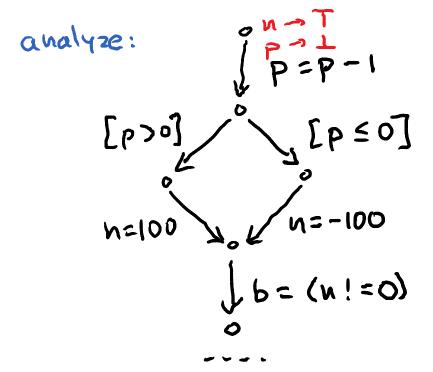
```
static void test(int p) {
       int n;
       p = p - 1;
       if (p > 0) {
         n = 100;
       System.out.println("Hello!");
       if (p > 0) {
         while (n != 0) {
            System.out.println(n);
            n = n - p;
```

Initialization Analysis

```
class Test {
  static void test(int p) {
          int n;
         p = p - 1;
         if (p > 0) {
            n = 100;
          else {
            n = -100;
          while (n != 0) {
            System.out.println(n);
            n = n - p;
```

T indicates presence of flow from states where variable was not initialized:

- If variable is possibly uninitialized, we use T
- Otherwise (initialized, or unreachable): ⊥



If var occurs anywhere but left-hand side of assignment and has value T, report error

Sketch of Initialization Analysis

- Domain: for each variable, for each program point:
 D = {\pm ,T}
- At program entry, local variables: T; parameters: 上
- At other program points: each variable: 上
- An assignment x = e sets variable x to \bot
- lub (join) of any value with T gives T
 - uninitialized values are contagious along paths
 - ⊥ value for x means there is definitely no possibility for accessing uninitialized value of x

Run initialization analysis Ex.1

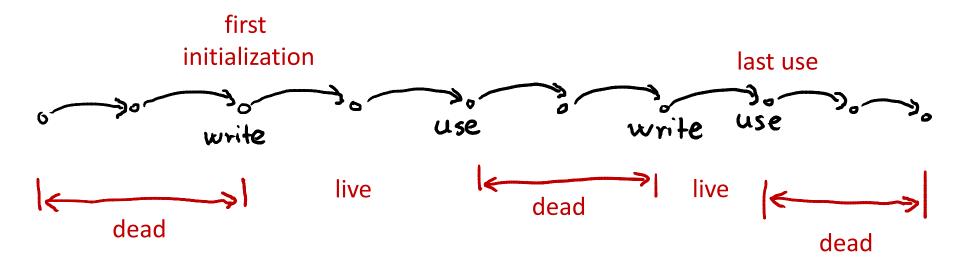
```
int n;
p = p - 1;
if (p > 0) {
  n = 100;
while (n != 0) {
  n = n - p;
```

Run initialization analysis Ex.2

```
int n;
p = p - 1;
if (p > 0) {
  n = 100;
if (p > 0) {
  n = n - p;
```

Liveness Analysis

Variable is dead if its current value will not be used in the future. If there are no uses before it is reassigned or the execution ends, then the variable is surely dead at a given point.



What is Written and What Read

$$x = y + x$$

written

if $(x > y)$

Example:

125 X=42 X=42 X=42 X=25 Y=X+3 Y=3+X X=3+X

Purpose:

Register allocation:

find good way to decide which variable should go to which register at what point in time.

How Transfer Functions Look

L- set of live variables

Generally

Initialization: Forward Analysis

```
while (there was change)
  pick edge (v1,statmt,v2) from CFG
      such that facts(v1) has changed
  facts(v2)=facts(v2) join transferFun(statmt, facts(v1))
}
```

Liveness: Backward Analysis

```
while (there was change)
  pick edge (v1,statmt,v2) from CFG
      such that facts(v2) has changed
  facts(v1)=facts(v1) join transferFun(statmt, facts(v2))
}
```

Example

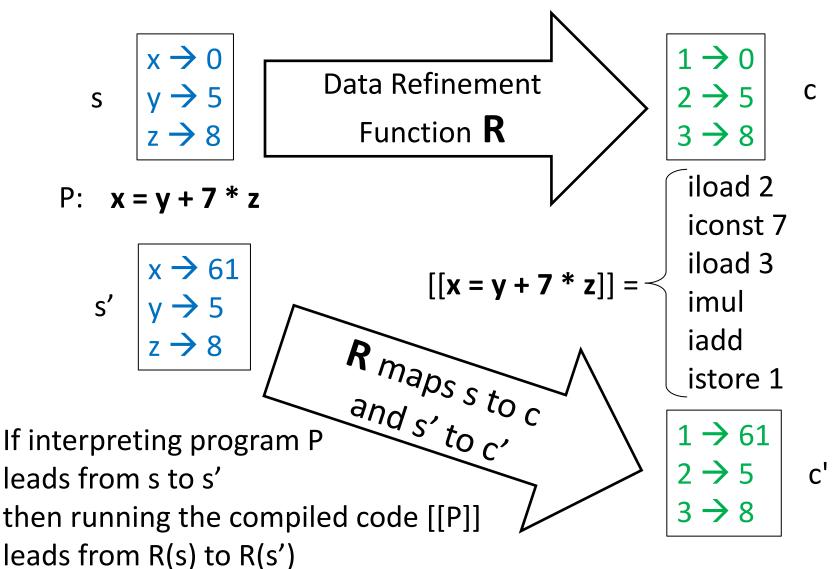
```
x = m[0]
y = m[1]
xy = x * y
z = m[2]
yz = y*z
xz = x*z
res1 = xy + yz
m[3] = res1 + xz
```

Data Representation Overview

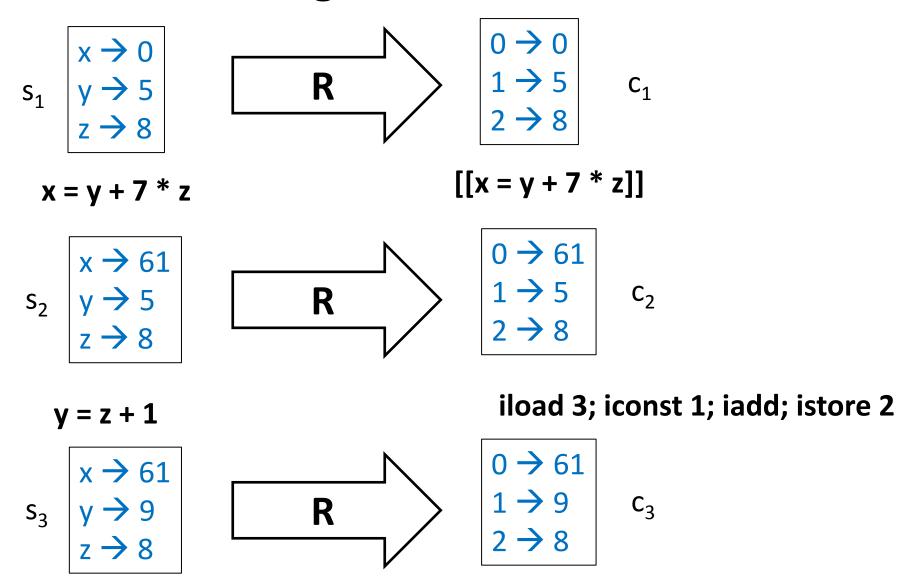
Original and Target Program have Different Views of Program State

- Original program:
 - local variables given by names (any number of them)
 - each procedure execution has fresh space for its variables (even if it is recursive)
 - fields given by names
- Java Virtual Machine
 - local variables given by slots (0,1,2,...), any number
 - intermediate values stored in operand stack
 - each procedure call gets fresh slots and stack
 - fields given by names and object references
- Machine code: program state is a large arrays of bytes and a finite number of registers

Compilation Performs Automated Data Refinement



Inductive Argument for Correctness



(R may need to be a relation, not just function)

A Simple Theorem

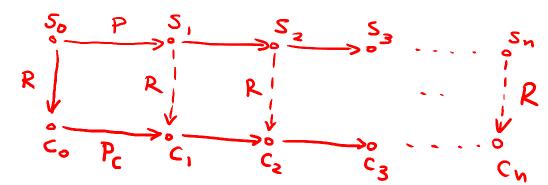
 $P: S \rightarrow S$ is a program meaning function

 $P_c: C \rightarrow C$ is meaning function for the compiled program

 $R: S \rightarrow C$ is data representation function

Let $s_{n+1} = P(s_n)$, n = 0,1,... be interpreted execution

Let $c_{n+1} = P(c_n)$, n = 0,1,... be compiled execution



Theorem: If

$$- c_0 = R(s_0)$$

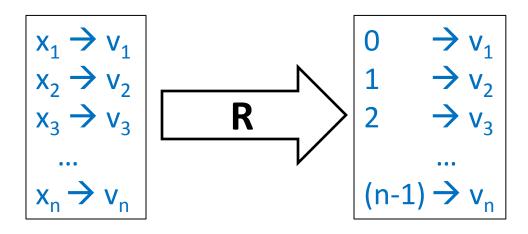
- for all s, $P_c(R(s)) = R(P(s))$

then $c_n = R(c_n)$ for all n.

Proof: immediate, by induction. R is often called **simulation relation.**

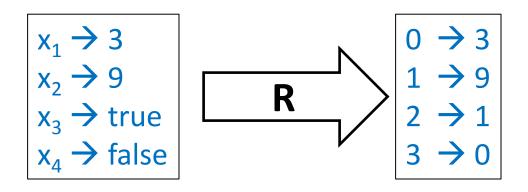
Example of a Simple R

- Let the receiver, the parameters, and local variables, in their order of declaration, be $X_{1,} X_{2} ... X_{n}$
- Then R maps program state with only integers like this:



R for Booleans

- Let the received, the parameters, and local variables, in their order of declaration, be $X_{1,} X_{2} ... X_{n}$
- Then R maps program state like this, where x_1 and x_2 are integers but x_3 and x_4 are Booleans:



R that depends on Program Point

```
\rightarrow V_1
def main(x:Int) {
                                 res \rightarrow v_2
 var res, y, z: Int
 if (x>0) {
   y = x + 1
   res = y
 } else {
                                 res \rightarrow v_2
    z = -x - 10
    res = z
 return res;
                                 res \rightarrow v_2
Map y,z to same slot.
Consume fewer slots!
```

Packing Variables into Memory

- If values are not used at the same time, we can store them in the same place
- This technique arises in
 - Register allocation: store frequently used values in a bounded number of fast registers
 - 'malloc' and 'free' manual memory management:
 free releases memory to be used for later objects
 - Garbage collection, e.g. for JVM, and .NET as well as languages that run on top of them (e.g. Scala)

Register Machines

Better for most purposes than stack machines

- closer to modern CPUs (RISC architecture)
- closer to control-flow graphs
- simpler than stack machine

Example: **ARM** architecture

Directly
Addressable
RAM
(large - GB,
slow, even
with caches)

A few fast registers

R0,R1,...,R31

Basic Instructions of Register Machines

```
R_i \leftarrow Mem[R_i] load
```

$$Mem[R_i] \leftarrow R_i$$
 store

$$R_i \leftarrow R_j * R_k$$
 compute: for an operation *

Efficient register machine code uses as few loads and stores as possible.

State Mapped to Register Machine

Both dynamically allocated heap and stack expand

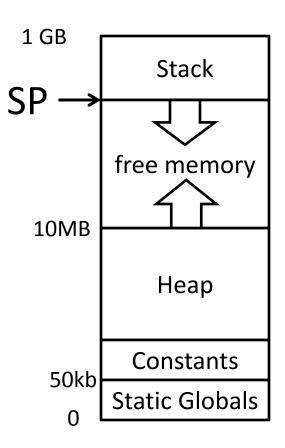
- heap need not be contiguous; can request more memory from the OS if needed
- stack grows downwards

Heap is more general:

- Can allocate, read/write, and deallocate, in any order
- Garbage Collector does deallocation automatically
 - Must be able to find free space among used one, group free blocks into larger ones (compaction),...

Stack is more efficient:

- allocation is simple: increment, decrement
- top of stack pointer (SP) is often a register
- if stack grows towards smaller addresses:
 - to allocate N bytes on stack (push): SP := SP N
 - to deallocate N bytes on stack (pop): SP := SP + N



Exact picture may depend on hardware and OS

JVM vs General Register Machine Code Naïve Correct Translation

JVM:

imul

Register Machine:

 $R1 \leftarrow Mem[SP]$

SP = SP + 4

 $R2 \leftarrow Mem[SP]$

R2 ← R1 * R2

 $Mem[SP] \leftarrow R2$

Register Allocation

How many variables?

x,y,z,xy,xz,res1

Do we need 6 distinct registers if we wish to avoid load and stores?

$$x = m[0]$$
 $x = m[0]$
 $y = m[1]$ $y = m[1]$
 $xy = x * y$ $xy = x * y$
 $z = m[2]$ $z = m[2]$
 $yz = y * z$ $yz = y * z$
 $xz = x * z$ $y = x * z$ // reuse y
 $x = xy + yz$ // reuse x
 $x = m[0]$

Idea of Graph Coloring

- Register Interference Graph (RIG):
 - indicates whether there exists a point of time where both variables are live
 - look at the sets of live variables at all progrma points after running live-variable analysis
 - if two variables occur together, draw an edge
 - we aim to assign different registers to such these variables
 - finding assignment of variables to K registers:
 corresponds to coloring graph using K colors

Graph Coloring Algorithm

Simplify

If there is a node with less than K neighbors, we will always be able to color it! so we can remove it from the graph
This reduces graph size (it is incomplete)
e.g.Every planar can be colored by at most 4 colors (yet can have nodes with 100 neighbors)

Spill

If every node has K or more neighbors, we remove one of them we mark it as node for potential spilling then remove it and continue

Select

Assign colors backwards, adding nodes that were removed

If we find a node that was spilled, we check if we are lucky that we can color it

if yes, continue

if no, insert instructions to save and load values from memory

restart with new graph (now we have graph that is easier to color, we killed a variable)

Run Register Allocation Ex.1 use 4 registers

```
x = m[0]
y = m[1]
xy = x * y
z = m[2]
yz = y*z
xz = x*z
res1 = xy + yz
m[3] = res1 + xz
```

Run Register Allocation Ex.2 use 3 registers

```
x = m[0]
y = m[1]
xy = x * y
z = m[2]
yz = y*z
xz = x*z
res1 = xy + yz
m[3] = res1 + xz
```

Conservative Coalescing

Suppose variables tmp1 and tmp2 are both assigned to the same register R and the program has an instruction:

tmp2 = tmp1

which moves the value of tmp1 into tmp2. This instruction then becomes

R = R

which can be simply omitted.

How to force a register allocator to assign tmp1 and tmp2 to same register? merge the nodes for tmp1 and tmp2 in the interference graph! this is called **coalescing**

But: if we coalesce non-interfering nodes when there are assignments, then our graph may become more difficult to color, and we may in fact need more registers!

Conservative coalescing: coalesce only if merged node of tmp1 and tmp2 will have a small degree so that we are sure that we will be able to color it.

Run Register Allocation Ex.3 use 4 registers, coallesce j=i

```
i = 0

s = s + i

i = i + b

j = i

s = s + j + b

j = j + 1
```