How Long Does Analysis Take?

Handling Loops: Iterate Until Stabilizes

How many steps until it stabilizes? Compute.

 $S_{1}^{2} C_{1}$ $x_{1}^{2} T_{1}^{2} n_{1}^{2} n_{1}^{2} T_{1}^{2} n_{1}^{2} T_{1}^$ ≈ 50'000 steps x = 1 n = 100000while (x < n) { $+ 0 \qquad + 1 \times (13), v$ [$\times < n$] [$\times > n$] x = x + 2

Handling Loops: Iterate Until Stabilizes How many steps until it stabilizes? $x: [0, +\infty) \quad x \ge 0$

```
var x : BigInt = 1
var n : BigInt = readInput()
while (x < n) {
    x = x + 2
}</pre>
```

For unknown program inputs and unbounded domains it may be practically impossible to know how long it takes.

Solutions

smaller domain, e.g. only certain intervals
 [a,b] where a,b in {-MI,-127,-1,0,1,127,MI-1}
 widening techniques (make it less precise on demand)

Smaller domain: intervals [a,b] where a,b∈{-MI,-127,0,127,MI-1} (MI denoted M)



Size of analysis domain

Interval analysis:

D₁ = { [a,b] | a ≤ b, a,b ∈ {-M,-127,-1,0,1,127,M-1}} U {⊥} Constant propagation:

$$D_{2} = \{ [a,a] \mid a \in \{-M, -(M-1), ..., -2, -1, 0, 1, 2, 3, ..., M-1\} \} \cup \{\perp, T\}$$

suppose M is 2⁶³
 $\circ entry$

$$|D_1| = 1 + (7 + 6 + 5 + ... + 1)$$

$$|D_2| = 1 + 2^{64} + 1$$

How many steps X= X² until it stabilizes, for any program with one variable?

$$x=1$$

$$\sum_{x=1}^{x=1} [!(x<10d] exit)$$

$$x:T y:T$$

$$[x<10d]$$

How many steps does the analysis take to finish (converge)?

Interval analysis:

 $D_1 = \{ [a,b] \mid a \le b, a,b \in \{-M,-127,-1,0,1,127,M-1\} \} \cup \{ \perp \}$ **Constant propagation:**

X= X+2



With D_1 takes at most $\overline{5}$ steps.

With D_2 takes at most 2 steps.

Chain of length n

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- A set of elements $x_0, x_1, ..., x_n$ in D that are linearly ordered, that is $x_0 \leq x_1 \leq ... \leq x_n$
- A lattice can have many chains. Its height is the maximum n for all the chains, if finite
- If there is no upper bound on lengths of chains, we say lattice has infinite height
- A monotonic sequence of distinct elements has length at most equal to lattice height

Termination Given by Length of Chains

Interval analysis:

 $D_1 = \{ [a,b] \mid a \le b, a,b \in \{-M,-127,-1,0,1,127,M-1\} \} \cup \{ \perp \}$ [-m,0] **Constant propagation:** $D_2 = \{ [a,a] \mid a \in \{-M, ..., -2, -1, 0, 1, 2, 3, ..., M-1\} \} \cup \{ \perp, T \}$ suppose M is 263 r-m."m-17 $[-M,-M] \qquad [-2,-2] [-1,-1] [0,0] [1,1] [2,2] ... [M-1,M-1]$

Product Lattice for All Variables

- If we have N variables, we keep one element for each of them
- This is like N-tuple of variables
- Resulting lattice is product of N lattices for
 individual variables
 x₁T₁ y₁ T

X: [0,0] 4: L

- Size is $|D|^{\mathbb{N}}$ $(2+2^{64})^{20}$
- The height is only N times the height of D Max height: $h(D) \cdot |V| \cdot |nodes| = 2 \cdot 20$ Nov height: $h(D) \cdot |V| \cdot |nodes| = 2 \cdot 20$

Summary and More Examples of Abstract Interpretation

Unbounded Range Analysis

Also called interval analysis Z - integers

 $\begin{array}{l} \mathsf{D} = \{\bot,\!\mathsf{T}\}\,\mathsf{U}\,\{\,[\mathsf{a},\!\mathsf{b}]\mid\,\mathsf{a},\!\mathsf{b}\!\in\!\!\mathbf{Z}\}\,\mathsf{U}\\ \{(\!-\!\infty,\!\mathsf{b}]\mid\,\mathsf{b}\!\in\!\!\mathbf{Z}\}\,\mathsf{U}\,\{\![\mathsf{a},\!\infty\!)\mid\,\mathsf{a}\!\in\!\!\mathbf{Z}\} \end{array}$

So domain values are:

- bounded intervals of integers [a,b]
- intervals unbounded from one side $(-\infty,b]$, $[a,\infty)$
- empty set \bot
- the set of all integers T

Convergence not ensured automatically – can increase intervals forever

even for e.g. 32-bit integers, convergence can take many steps

Range Analysis of N-bit integers

$$\begin{split} \textbf{B}_{32} &- 32\text{-bit integers} \\ \textbf{D} &= \{\bot, T\} \cup \{ [a,b] \mid a,b \in \textbf{B}_{32} \} \cup \\ & \{(-\infty,b] \mid b \in \textbf{B}_{32} \} \cup \{ [a,\infty) \mid a \in \textbf{B}_{32} \} \end{split}$$

What is the height of the lattice for 3 variables and 7 program points?

Constant Propagation

Special case of interval analysis:

D = { [a,a] | $a \in \{..., -2, -1, 0, 1, 2, 3, ...\}$ U { \perp ,T} Write [a,a] simply as a. So values are:

- a known constant at this point: a
- "we could not show it is constant": T
- "we did not reach this program point":
 Convergence fast lattice has small height



For each variable (x,y,z) we store a constant \perp , or T

table for +:



abstract class Element case class Top extends Element case class Bot extends Element **case class** Const(v:Int) extends Element **var** facts : Map[Nodes,Map[VarNames,Element]] what executes during analysis: oldY = facts(v_1)("y") $oldZ = facts(v_1)("z")$ newX = tableForPlus(oldY, oldZ) $facts(v_2) = facts(v_2)$ join $facts(v_1).updated("x", newX)$

Initialization Analysis



What does javac say to this:

class Test {

static void test(int p) {

```
int n;
p = p - 1;
if (p > 0) {
  n = 100;
}
while (n != 0) {
  System.out.println(n);
  n = n - p;
}
              Test.java:8: variable n might not have been initialized
                       while (n > 0) {
                       Λ
              1 error
```

Program that compiles in java

class Test {

}

```
static void test(int p) {
       int n;
       p = p - 1;
       if (p > 0) {
          n = 100;
       }
       else {
          n = -100;
       }
       while (n != 0) {
          System.out.println(n);
          n = n - p;
       }
```

} // Try using if (p>0) second time.

We would like variables to be initialized on all execution paths.

Otherwise, the program execution could be undesirable affected by the value that was in the variable initially.

We can enforce such check using initialization analysis.

Initialization Analysis

class Test {

}

```
static void test(int p) {
       int n;
       p = p - 1;
       if (p > 0) {
          n = 100;
       }
       else {
          n = -100;
       }
       while (n != 0) {
          System.out.println(n);
          n = n - p;
       }
```

T indicates presence of flow from states where variable was not initialized:

- If variable is possibly uninitialized, we use T
- Otherwise (initialized, or unreachable): ⊥



If var occurs anywhere but left-hand side of assignment and has value T, report error

} // Try using if (p>0) second time.

Liveness Analysis

Variable is dead if its current value will not be used in the future. If there are no uses before it is reassigned or the execution ends, then the variable is sure dead at a given point.



What is Written and What Read

 $\mathbf{x} = \mathbf{y} + \mathbf{x}$ written if (x > y)

Example:



Purpose:

Register allocation: find good way to decide which variable should go to which register at what point in time.

How Transfer Functions Look

$$L_{c}^{-}$$
 set of live variables
 $\int_{c}^{1} \sum_{x=x+Y}^{2} \sum_{y=1}^{1} \sum_{x=1}^{2} \sum_{x=1}^{2} \sum_{y=1}^{2} \sum_{y=1}^{2}$

$$L_{0} = (L_{2} \setminus \{x\}) \cup \{x,y\}$$

Generally $L_0 = (L_2 \setminus def(st)) \cup use(st)$

Initialization: Forward Analysis

```
while (there was change)
  pick edge (v1,statmt,v2) from CFG
      such that facts(v1) has changed
  facts(v2)=facts(v2) join transferFun(statmt, facts(v1))
}
```

Liveness: Backward Analysis

while (there was change)
 pick edge (v1,statmt,v2) from CFG
 such that facts(v2) has changed
 facts(v1)=facts(v1) join transferFun(statmt, facts(v2))
}

Example

Data Representation Overview

Original and Target Program have Different Views of Program State

- Original program:
 - local variables given by names (any number of them)
 - each procedure execution has fresh space for its variables (even if it is recursive)
 - fields given by names
- Java Virtual Machine
 - local variables given by slots (0,1,2,...), any number
 - intermediate values stored in operand stack
 - each procedure **call** gets fresh slots and stack
 - fields given by names and object references
- Machine code: program state is a large arrays of bytes and a finite number of registers

Compilation Performs Automated Data Refinement



Inductive Argument for Correctness



(R may need to be a relation, not just function)

A Simple Theorem





Theorem: If

 $- c_0 = R(s_0)$

- for all s,
$$P_c(R(s)) = R(P(s))$$

then $c_n = R(c_n)$ for all n.

Proof: immediate, by induction. R is often called **simulation relation**.

Example of a Simple R

- Let the received, the parameters, and local variables, in their order of declaration, be
 X₁, X₂ ... X_n
- Then R maps program state with only integers like this:



R for Booleans

- Let the received, the parameters, and local variables, in their order of declaration, be
 X₁, X₂ ... X_n
- Then R maps program state like this, where x₁ and x₂ are integers but x₃ and x₄ are Booleans:



R that depends on Program Point



Packing Variables into Memory

- If values are not used at the same time, we can store them in the same place
- This technique arises in
 - Register allocation: store frequently used values in a bounded number of fast registers
 - 'malloc' and 'free' manual memory management:
 free releases memory to be used for later objects
 - Garbage collection, e.g. for JVM, and .NET as well as languages that run on top of them (e.g. Scala)

Register Machines

Better for most purposes than stack machines

- closer to modern CPUs (RISC architecture)
- closer to control-flow graphs
- simpler than stack machine

Example: <u>ARM architecture</u>

Directly
Addressable
RAM
(large - GB,
slow)

A few fast registers

R0,R1,,R31

Basic Instructions of Register Machines

 $R_i \leftarrow Mem[R_j]$ load $Mem[R_j] \leftarrow R_i$ store $R_i \leftarrow R_j * R_k$ compute: for an operation *

Efficient register machine code uses as few loads and stores as possible.

State Mapped to Register Machine

Both dynamically allocated heap and stack expand

- heap need not be contiguous can request more memory from the OS if needed
- stack grows downwards

Heap is more general:

- Can allocate, read/write, and deallocate, in any order
- Garbage Collector does deallocation automatically
 - Must be able to find free space among used one, group free blocks into larger ones (compaction),...

Stack is more efficient:

- allocation is simple: increment, decrement
- top of stack pointer (SP) is often a register
- if stack grows towards smaller addresses:
 - to allocate N bytes on stack (push): SP := SP N
 - to deallocate N bytes on stack (pop): SP := SP + N



Exact picture may depend on hardware and OS

JVM vs General Register Machine Code

JVM: imul **Register Machine:**

- R1 ← Mem[SP]
- SP = SP + 4
- $R2 \leftarrow Mem[SP]$
- R2 ← R1 * R2
- Mem[SP] ← R2

Register Allocation

How many variables? x,y,z,xy,xz,res1

Do we need 6 distinct registers if we wish to avoid load and stores?

x = m[0]	x = m[0]	
y = m[1]	y = m[1]	
xy = x * y	xy = x * y	
z = m[2]	z = m[2]	
$yz = y^*z$	$yz = y^*z$	
$xz = x^*z$	$\mathbf{y} = \mathbf{x}^* \mathbf{z}$	// reuse y
res1 = xy + yz	$\mathbf{x} = \mathbf{x}\mathbf{y} + \mathbf{y}\mathbf{z}$	// reuse x
m[3] = res1 + xz	m[3] = x + y	

Idea of Graph Coloring

- Register Interference Graph (RIG):
 - indicates whether there exists a point of time where both variables are live
 - if so, we draw an edge
 - we will then assign different registers to these variables
 - finding assignment of variables to K registers corresponds to coloring graph using K colors!

Graph Coloring Algorithm

Simplify

If there is a node with less than K neighbors, we will always be able to color it! so we can remove it from the graph This reduces graph size (it is incomplete)

Every planar can be colored by at most 4 colors (yet can have nodes with 100 neighbors)

Spill

If every node has K or more neighbors, we remove one of them we mark it as node for potential spilling then remove it and continue

Select

Assign colors backwards, adding nodes that were removed If we find a node that was spilled, we check if we are lucky that we can color it if yes, continue if no, insert instructions to save and load values from memory restart with new graph (now we have graph that is easier to color, we killed a variable)

Examples